# Lower Bounds for Tropical Circuits and Dynamic Programs\*

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#### **Abstract**

Tropical circuits are circuits with Min and Plus, or Max and Plus operations as gates. Their importance stems from their intimate relation to dynamic programming algorithms. The power of tropical circuits lies somewhere between that of monotone boolean circuits and monotone arithmetic circuits. In this paper we present some lower bounds arguments for tropical circuits, and hence, for dynamic programs.

**Keywords:** Tropical circuits; dynamic programming; monotone arithmetic circuits; lower bounds

#### 1 Introduction

Understanding the power and limitations of fundamental algorithmic paradigms—such as greedy or dynamic programming—is one of the basic questions in the algorithm design and in the whole theory of computational complexity. In this paper we focus on the dynamic programming paradigm.

Our starting point is a simple observation that many dynamic programming algorithms for optimization problems are just recursively constructed *circuits* over the corresponding semirings. Each such circuit computes, in a natural way, some polynomial over the underlying semiring. Most of known dynamic programming algorithms correspond to circuits over the (min,+) or (max,+) semirings, that is, to *tropical circuits*.¹ Thus, lower bounds for tropical circuits show the limitations of dynamic programming algorithms over the corresponding semirings.

The power of tropical circuits (and hence, of dynamic programming) lies somewhere between that of monotone boolean circuits and monotone arithmetic circuits:

monotone boolean ≤ tropical ≤ monotone arithmetic

and the gaps may be even exponential (we will show this in Section 7).

Monotone *boolean* circuits are most powerful among these three models and, for a long time, only linear lower bounds were known for such circuits. First super-polynomial lower bounds for the k-clique function CLIQUE and the perfect matching function PER were proved by Razborov [37, 36] by inventing his method of approximations. At almost about the same time, explicit exponential lower bounds were also proved by Andreev [3, 4]. Alon and Boppana [1] improved Razborov's lower bound for CLIQUE from super-polynomial until exponential. Finally, Jukna [17] gave a general and easy

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<sup>&</sup>lt;sup>1</sup>There is nothing special about the term "tropical". Simply, this term is used in honor of Imre Simon who lived in Sao Paulo (south tropic). Tropical algebra and tropical geometry are now intensively studied topics in mathematics.

to apply lower bounds criterium for monotone boolean and real-valued circuits, yielding strong lower bounds for a row of explicit boolean functions. These lower bounds hold for tropical circuits as well.

On the other hand, monotone *arithmetic* circuits are much easier to analyze: such a circuit cannot produce anything else but the monomials of the computed polynomial, no "simplifications" (as  $x^2 = x$  or x + xy = x) are allowed here. Exponential lower bounds on the monotone arithmetic circuit complexity were proved already by Schnorr [38] (for CLIQUE), and Jerrum and Snir [15] (for PER and some other polynomials). A comprehensive survey on arithmetic (not necessarily monotone) circuits can be found in the book by Shpilka and Yehudayoff [41].

In this paper we summarize our knowledge about the power of tropical circuits. As far as we know, no similar attempt was undertaken in this direction after the classical paper by Jerrum and Snir [15]. The main message of the paper is that not only methods developed for monotone *boolean* circuits, but (sometimes) even those for a much weaker model of monotone *arithmetic* circuits can be used to establish limitations of dynamic programming. Although organized as a survey, the paper contains some new results, including:

- 1. A short and direct proof that tropical circuits for optimization problems with *homogeneous* target polynomials are not more powerful than monotone arithmetic circuits (Theorem 13). This explains why we do not have efficient dynamic programming algorithms for optimization problems whose target sums all have the same length.
- 2. A new and simple proof of Schnorr's [38] lower bound on the size of monotone arithmetic circuits computing so-called "separated" polynomials (Theorem 24). A polynomial *f* is *separated* if the product of any two of its monomials contains no third monomial of *f* distinct from these two ones.
- 3. A new and simpler proof of Gashkov and Sergeev's [10, 12] lower bound on the size of monotone arithmetic circuits computing so-called "k-free" polynomials (Theorem 34). A polynomial is *k-free* if it does not contain a product of two polynomials, both with more than *k* monomials. This extend's Schnorr's bound, since every separated polynomial is also 1-free.
- 4. An easy to apply "rectangle" lower bound (Lemma 42).
- 5. A truly exponential lower bound for monotone arithmetic circuits using expander graphs (Theorem 48).

### 2 Semirings and Polynomials

In this section, we introduce the (fairly standard) algebraic terminology we shall subsequently use.

A (commutative) semiring is a system S = (S, +, \*, 0, 1), where S is a set, + ("sum") and \* ("product") are binary operations on S, and 0 and 1 are elements of S having the following three two properties:

- (i) in both (S, +, 0) and (S, \*, 1), operation are associative and commutative with identities 0 and 1: a + 0 = a and a\*1 = a hold for all  $a \in S$ ;
- (ii) product distributes over sum: a\*(b+c) = (a\*b) + (a\*c).

Some authors also add the "annihilation axiom" a\*0 = 0 for all  $a \in S$ ; we will not require it to hold. A semiring is *additively idempotent* if a + a = a holds for all  $a \in S$ , and is *multiplicatively idempotent* 

if a\*a = a holds for all  $a \in S$ . We will use the common conventions to save parenthesis by writing a\*b + c\*d instead of (a\*b) + (a\*c), and replacing a\*b by ab. Also,  $a^n$  will stand for  $a*a*\cdots*a$  n-times.

In this paper, we will be interested in the following semirings:

- Arithmetic semiring  $A = (N, +, \cdot, 0, 1)$ .
- Boolean semiring B =  $(\{0,1\}, \vee, \wedge, 0, 1)$ .
- Min semirings Min =  $(N, \min, +, \infty, 0)$  and  $Min^- = (Z, \min, +, \infty, 0)$ .
- Max semirings Max = (N, max, +, 0, 0) and  $Max^* = (N, max, +, -\infty, 0)$ .
- Min and Max semirings are called *tropical* semirings.

Note that all these semirings, but Max, satisfy the annihilation axiom, all but A, are additively idempotent, and none of them, but B, is multiplicatively idempotent. Note also that the only difference of Max\* from Max is that Max\* contains one additional "annihilating" element  $-\infty$  satisfying  $\max\{-\infty, a\} = a$  and  $-\infty + a = -\infty$  for all  $a \in \mathbb{N}$ . The difference of Min<sup>-</sup> from Min is that Min<sup>-</sup> also contains negative integers.

In arithmetic and in tropical semirings one usually allows rational or even real numbers, not just integers. This corresponds to considering optimization problems with real, not necessarily integral "weights". The point, however, is that lower-bound techniques, we will consider below, work already on smaller domains: it will be enough that Min contains  $\{0,1,+\infty\}$ , Min<sup>-</sup> contains  $\{-1,0,+\infty\}$ , Max contains  $\{0,1\}$ , and Max\* contains  $\{0,1,-\infty\}$ .

Due to their intimate relation to discrete optimization, we will be mainly interested in tropical semirings, and circuits over these semirings. Lower bounds for such circuits give lower bounds for the number of subproblems used by dynamic programming algorithm.

Let S = (S, +, \*, 0, 1) be a semiring, and let  $x_1, \ldots, x_n$  be variables ranging over S. A monomial is any product of these variables, where repetitions are allowed. By commutativity and associativity, we can sort the products and write monomials in the usual notation, with the variables raised to exponents. Thus, every monomial  $x_1^{a_1} x_2^{a_2} \cdots x_n^{a_n}$  is uniquely determined by the vector of exponents  $(a_1, \ldots, a_n) \in \mathbb{N}^n$ , where  $x_i^0 = 1$ . (Note that in tropical semirings, monomials are linear combinations  $a_1x_1 + a_2x_2 + \cdots + a_nx_n$ , that is, sums, not products.) The degree, |p|, of a monomial is the sum  $|p| = a_1 + \cdots + a_n$  of its exponents. A monomial p is multilinear if every exponent  $a_i$  is either 0 or 1. A monomial  $p = x_1^{a_1} \cdots x_n^{a_n}$  contains a monomial  $q = x_1^{b_1} \cdots x_n^{b_n}$  (or q is a factor of p) if  $a_i \ge b_i$  for all  $i = 1, \ldots, n$ , that is, if p = qq' for some monomial q'.

By a *polynomial*<sup>2</sup> we will mean a finite sum of monomials, where repetitions of monomials are allowed. That is, we only consider polynomials with nonnegative integer coefficients. A polynomial is *homogeneous* if all its monomials have the same degree, and is *multilinear* if all its monomials are multilinear (no variables of degree > 1). For example,  $f = x^2y + xyz$  is homogeneous but not multilinear, whereas g = x + yz is multilinear but not homogeneous. The sum and product of two polynomials is defined in the standard way. For polynomials f, h and a monomial p, we will write:

- f = h if f and h have the same monomials appearing not necessarily with the same coefficients;
- $f \rightleftharpoons h$  if f and h have the same monomials appearing with the same coefficients;
- $f \subseteq h$  if every monomial of f is also a monomial of h;

<sup>&</sup>lt;sup>2</sup>Usually, polynomials of more than one variable are called *multivariate*, but we will omit this for shortness.

- $p \in f$  if p is a monomial of f;
- |f| to denote the number of *distinct* monomials in f;
- $X_p$  to denote the set of variables appearing in p with non-zero degree;

Every polynomial  $f(x_1,...,x_n)$  defines a function  $\hat{f}:S^n\to S$ , whose value  $\hat{f}(s_1,...,s_n)$  is obtained by substituting elements  $s_i\in S$  for  $x_i$  in f. Polynomials f and g are equivalent (or represent the same function) over a given semiring, if  $\hat{f}(s)=\hat{h}(s)$  holds for all  $s\in S^n$ . It is important to note that the same polynomial  $f(x)\rightleftharpoons\sum_{I\in I}c_I\prod_{i\in I}x_i^{a_i}$  represents different functions over different semirings:

$$\hat{f}(x) = \sum_{I \in I} c_I \prod_{i \in I} x_i^{a_i} \qquad \text{over A (counting)}$$

$$\hat{f}(x) = \bigvee_{I \in I} \bigwedge_{i \in I} x_i \qquad \text{over B (existence)}$$

$$\hat{f}(x) = \min_{I \in I} \sum_{i \in I} a_i x_i \qquad \text{over Min and Min}^- \text{ (minimization)}$$

$$\hat{f}(x) = \max_{I \in I} \sum_{i \in I} a_i x_i \qquad \text{over Max and Max}^+ \text{ (maximization)}$$

$$\hat{f}(x) = \max_{I \in I} \min_{i \in I} x_i \qquad \text{over B}^+ = (\mathsf{N} \cup \{+\infty\}, \max, \min, 0, +\infty) \text{ (bottleneck)}$$

Note that, due to their additive idempotence, in the boolean semiring, bottleneck semiring as well as in all four tropical semirings, the coefficients  $c_I$  (repetitions of monomials) do not influence the computed value  $\hat{f}(x)$ , and we can assume that  $c_I = 1$  for all  $I \in I$ ; this is because, say,  $\min\{x, x, y\} = \min\{x, y\}$ . The degrees, however, are important: say,  $\min\{2x, y\} \neq \min\{x, y\}$ . In boolean and bottleneck semirings, neither coefficients nor degrees are important.

## 3 Circuits and their Polynomials

A *circuit* F over a semiring S = (S, +, \*, 0, 1) is a usual fanin-2 circuit whose inputs are variables  $x_1, \ldots, x_n$  and constants 0 and 1. Gates are fanin-2 + and \*. That is, we have a directed acyclic graph with n + 2 fanin-0 nodes labeled by  $x_1, \ldots, x_n, 0, 1$ . At every other node, the sum (+) or the product (\*) of its entering nodes is computed; nodes with assigned operations are called *gates*. The *size* of F, denoted by Size(F), is the number of gates in F. The *depth* is the largest number of edges in a path from an input gate to an output gate.

Like polynomials, circuits are also syntactic objects. So, we can associate with every circuit F the unique polynomial *F produced* by F inductively as follows:<sup>3</sup>

- If  $F = x_i$ , then  $F \rightleftharpoons x_i$ .
- If F = G + H, then  $F \rightleftharpoons \sum_{p \in G} p + \sum_{q \in H} q$ .
- If F = G\*H, then  $F \rightleftharpoons \sum_{p \in G} \sum_{q \in H} pq$ .

 $<sup>^3</sup>$ We will always denote circuits as upright letters F,G,H,..., and their produced polynomials by italic versions  $F,G,H,\ldots$ 

When producing the polynomial F from a circuit F we only use the generic semiring axioms (i)–(iii) to write the result as a polynomial (sum of monomials). For example, if F = x\*(1 + y) then F = x + xy, even though  $\hat{F} = x$  in B and Min, and  $\hat{F} = xy$  in Max. It is thus important to note that the produced by a given circuit F polynomial F is the same over *any* semiring!

A circuit is *homogeneous*, if polynomials produced at its gates are homogeneous. It is easy to see that a circuit is homogeneous if and only if the polynomial produced by it is homogeneous. A circuit is *multilinear*, if for every its product gate u = v\*w, the sets of variables of the polynomials produced at gates v and w are disjoint. Sometimes, multilinear (in our sense) circuits are called also *syntactically multilinear*.

**Definition 1.** A circuit F *computes* a polynomial f if  $\hat{F} = \hat{f}$  (F and f coincide as functions). A circuit F *produces* f if F = f (F and f have the same set of monomials).

We will be interested in the following two complexity measures of polynomials f, where the third measure is only for multilinear polynomials:

- S(f) = minimum size of a circuit over semiring S computing f.
- S[f] = minimum size of a circuit over semiring S producing f.
- $S_{lin}(f)$  = minimum size of a *multilinear* circuit over semiring S computing f.

What we are really interested in is the first measure S(f). The second measure S[f] is less interesting: it is the *same* for all semirings S, because the formal polynomial of a given (fixed) circuit is the same over all semirings. In particular, we have that S[f] = A[f] holds for every semiring S and every polynomial f.

To illustrate the lower-bounds arguments, we will use the following popular polynomials. Variables  $x_e$  of considered polynomials correspond to edges e of a complete undirected n-vertex graph  $K_n$ , a complete bipartite  $n \times n$  graph  $K_{n,n}$ . Thus, monomials  $\prod_{e \in E} x_e$  correspond to some subgraphs E of  $K_n$  or  $K_{n,n}$ . Here are some of the polynomials we will use later:

- 1. Permanent polynomial PER<sub>n</sub> = all perfect matchings in  $K_{n,n}$ .
- 2. Hamiltonian cycle polynomial  $HC_n$  = all Hamiltonian cycles in  $K_n$ .
- 3. k-clique polynomial  $CLIQUE_{n,k} = all k$ -cliques in  $K_n$ .
- 4. Spanning tree polynomial  $ST_n$  = all spanning trees in  $K_n$  rooted in node 1.
- 5. st-connectivity polynomial STCON<sub>n</sub> = all paths from s = 1 to t = n in  $K_n$ .
- 6. st-walk polynomial WALK<sub>n</sub> = all walks from s = 1 to t = n of length at most n 1 in  $K_n$ .
- 7. All-pairs connectivity "polynomial" APSP<sub>n</sub> = set of  $\binom{n}{2}$  polynomials STCON<sub>n</sub> corresponding to different pairs of start and target nodes s and t.
- 8. Matrix product polynomial  $MP_n$  = special case of  $APSP_n$  when only paths of length-2 are considered.
- 9. The connectivity polynomial  $CONN_n$  = product of all polynomials of  $APSP_n$ .

In Section 11 we will show that the first four polynomials require Min-circuits of exponential size, whereas the next result shows that the last five polynomials all have Min-circuits of polynomial size. This result—proved independently by Moore [30], Floyd [7], and Warshall [45]—holds for every semiring with the absorption axiom a + ab = a, including the boolean and Min semirings.

**Theorem 2** ([30, 7, 45]). Over semirings Min and B, the polynomials of APSP<sub>n</sub> can all be simultaneously computed by a circuit of size  $O(n^3)$ .

Proof. Inputs for APSP<sub>n</sub> over the Min semiring are non-negative weights  $x_{ij}$  of the edges of  $K_n$ . For every pair i < j of distinct nodes of  $K_n$ , the goal is to compute the weight of the lightest path between i and j; the weight of a path is the sum of weights of its edges. The idea is to recursively compute the polynomials  $f_{i,j}^{[k]}$  for  $k = 0,1,\ldots,n$ , whose value is the weight of the lightest walk between i and j whose all inner nodes lie in  $[k] = \{1,\ldots,k\}$ . Then  $f_{i,j}^{[0]} = x_{ij}$ , and the recursion is:  $f_{i,j}^{[k]} = \min \left\{ f_{i,j}^{[k-1]}, f_{i,k}^{[k-1]} + f_{k,j}^{[k-1]} \right\}$ . The output gates are  $f_{i,j}^{[n]}$  for all i < j. The total number of gates is  $O(n^3)$ . Even though the circuit actually searches for weights of lightest walks, it correctly computes APSP because every walk between two nodes i and j also contains a simple path (with no repeated nodes) between these nodes. Since the weights are non-negative, the minimum must be achieved on a simple path. If we replace min-gates by OR-gates, and sum-gates by AND-gates, then the resulting circuit will compute APSP<sub>n</sub> over the boolean semiring B.

**Remark 3.** Theorem 2 immediately implies that the polynomials  $MP_n$ ,  $CONN_n$ , and  $STCON_n$  can also be computed by Min-circuits of size  $O(n^3)$ . Moreover, over the boolean semiring, the spanning tree polynomial ST represents the same boolean function as CONN. Thus, Theorem 2 also gives  $B(ST_n) = O(n^3)$ .

A dynamic programming algorithm of Bellman [6] and Ford [9] implies that the st-walk polynomial WALK<sub>n</sub> can even be *produced* by a small circuit.

**Theorem 4** ([6, 9]). *If*  $f = \text{WALK}_n$ , then  $A[f] = O(n^3)$ .

*Proof.* Let  $f_j^{[k]}$  be a polynomial whose monomials correspond to all walks from 1 to j of length at most k. Hence,  $f_j^{[1]} = x_{1j}$  for all j > 1, and  $f_n^{[n-1]} = \text{WALK}_n$ . The polynomial  $f_j^{[k]}$  is the sum of the polynomial  $f_j^{[k-1]}$  and all polynomials  $x_{i,j} \cdot f_i^{[k-1]}$  over all nodes  $i \neq j$ . The resulting circuit has  $O(n^3)$  fanin-2 gates.

**Remark 5.** Over boolean and Min semirings, the polynomials WALK and STCON compute the same function. But a relatively simple argument implies that  $A[g] = 2^{\Omega(n)}$  holds for  $g = STCON_n$  (Theorem 45 below). Thus, we have two polynomials f and g such that  $\hat{f} = \hat{g}$  over B and Min, but one of them requires exponentially larger circuits to be produced.

In the rest of the paper, we will present various lower bound argument for tropical circuits. Table 1 summarizes the resulting specific bounds obtained by these arguments for the polynomials listed above.

## 4 Structure of Produced Polynomials

If a circuit F computes some given polynomial f over some semiring S, that is, if  $\hat{F} = \hat{f}$  holds over S, what can we say about the *structure* of the polynomial F produced by the circuit? In this section, we summarize this information for various semirings.

Polynomial $f$	Bound	Reference
$ST_n$	$B(f) = O(n^3), S(f) = 2^{\Omega(n)}$	Rem. 3, Thm. 43
$CONN_n$ , $STCON_n$	$Min(f) = O(n^3), A[f] \geqslant Max(f) = 2^{\Omega(n)}$	Rem. 3
$APSP_n, MP_n$	$Min(f) = \Theta(n^3)$	Cor. 28
$PER_n, HC_n$	$S(f) = 2^{\Omega(n)}$	Thm. 43
$\mathrm{CLIQUE}_{n,k}$	$S(f) \geqslant {n \choose k} - 1$	Cor. 27

Table 1: Summary of specific bounds; S(f) stands for any of Min(f), Max(f) and  $B_{lin}(f)$ .

In general, neither  $F \rightleftharpoons f$  not F = f needs to hold. The arithmetic semiring, as well as tropical semirings Min<sup>-</sup> and Max\* are here an exception.

**Lemma 6.** If a circuit F computes a polynomial f over the arithmetic semiring A, then  $F \rightleftharpoons f$ .

*Proof.* There are several ways to prove this fact. We follow an elegant argument suggested by Sergey Gashkov (personal communication). If  $\hat{F} = \hat{f}$  but F and f do not coincide as polynomials, the polynomial g = F - f must contain at least one monomial. Let p be a monomial of g of maximum degree. Take all (formal) partial derivatives of g with respect to the variables of p until all they disappear. Since p has maximum degree, we obtain some constant  $\neq 0$ . But since  $\hat{g} = \hat{F} - \hat{f}$  is the zero function, the derivative should be zero, a contradiction.

**Lemma 7.** If a circuit F computes a multilinear polynomial f over Max, Min $^-$  or Max $^*$ , then F must also be multilinear. Moreover, over Min $^-$  and Max $^*$ , we have F = f.

*Proof.* Let us first show that the polynomial F produced by F must be also multilinear. To see this, assume that F contains a monomial p (sum) in which some variable  $x_i$  appears more than once. Then, in the semirings Max or Max\*, we can set  $x_i$  to 1, and set all the remaining variables to 0. Under this assignment a, we will have  $\hat{F}(a) \ge 2$  and  $\hat{f}(a) \le 1$ , a contradiction with  $\hat{F} = \hat{f}$ . In the Minsemiring, we can set  $x_i$  to -1, and set all the remaining variables to 0. Under this assignment b, we will have  $\hat{f}(b) \ge -1$ , because all monomials of f get value  $\ge -1$ , but  $\hat{F}(b) \le -2$  since already the monomial p of F gets value  $\le -2$ , a contradiction. Thus, both F and f must be multilinear.

Let us now show that F = f must hold over the semiring Min<sup>-</sup>; the argument for Max\* is similar. (This was proved by Jerrum and Snir [15] using the Farkas lemma. We give a direct proof.) We know that  $\hat{F} = \hat{f}$ , and that both polynomials F and f are multilinear. The main property of multilinear monomials p is that they are uniquely determined by their sets  $X_p$  of variables: if  $X_p = X_q$ , then p = q. Thus,  $f \nsubseteq F$  can only happen, if there is a monomial  $p \in f$  such that, for every monomial  $q \in F$ , we have that either  $X_q \nsubseteq X_p$  or  $X_q \subset X_p$  (proper inclusion). Let a be an assignment which sets all variables in  $X_p$  to -1, and the rest to  $\infty$ . Then  $\hat{f}(a) \leqslant \hat{p}(a) = -|X_p|$ . But for every monomial  $q \in F$ , we have either  $\hat{q}(a) = +\infty$ , if  $X_q \nsubseteq X_p$ , or  $\hat{q}(a) \geqslant -|X_p| + 1$ , if  $X_q \subset X_p$ . In any case, we have that  $\hat{F}(a) > \hat{f}(a)$ , a contradiction with  $\hat{F} = \hat{f}$ . This shows  $f \subseteq F$ . The proof of the converse inclusion  $F \subseteq f$  is the same. In the case of the Max\* semiring, it is enough to set all variables in  $X_p$  to 1, and the rest to  $-\infty$ .

**Remark 8.** Note that for non-multilinear polynomials, Lemma 7 needs not to hold. For example, if  $F = \min\{x, 2x, 3x\}$  and  $f = \min\{x, 3x\}$ , then  $\hat{F} = \hat{f}$  holds over Min<sup>-</sup>, but  $F \neq f$ .

In tropical semirings Min and Max, we only have weaker structural properties. For a polynomial f, let  $f_{\min} \subseteq f$  denote the set of all monomials of f not containing any other monomial of f, and  $f_{\max} \subseteq f$  denote the set of all monomials of f not contained in any other monomial of f. For example, if  $f = \{x, x^2y, yz\}$ , then  $f_{\min} = \{x, yz\}$  and  $f_{\max} = \{x^2y, yz\}$ . Note that every monomial of f contains (properly or not) at least one monomial of  $f_{\min}$ , and is contained in at least one monomial of  $f_{\max}$ . Note also that  $\hat{f}_{\min} = \hat{f}$  holds in Min semirings, and  $\hat{f}_{\max} = \hat{f}$  holds in Max semirings.

**Lemma 9.** If a circuit F computes a polynomial f over Min, and if  $f_{min}$  is multilinear, then  $F_{min} = f_{min}$ .

*Proof.* Let us first show that every monomial of F must contain at least one monomial of  $f_{\min}$ . For this, assume that there is a monomial  $p \in F$  which contains no monomial of  $f_{\min}$ . Since  $f_{\min}$  is multilinear, this implies that every monomial of  $f_{\min}$  must contain a variable not in  $X_p$ . So, on the assignment a which sets to 0 all variables in  $X_p$ , and sets to  $+\infty$  all the remaining variables, we have that  $\hat{f}(a) = \hat{f}_{\min}(a) = +\infty$ . But  $\hat{F}(a) \leqslant \hat{p}(a) = 0$ , a contradiction with  $\hat{F} = \hat{f}$ .

Thus, every monomial of F must contain at least one monomial of  $f_{\min}$ . Since no monomial in  $F_{\min}$  can contain another monomial of F, it remains to show that  $f_{\min} \subseteq F$ . For this, assume that there is a monomial  $q \in f_{\min}$  such that  $q \notin F$ . Take an assignment a which sets to 1 all variables in  $X_q$ , and sets to  $+\infty$  all the remaining variables. Then  $\hat{f}(a) \leqslant \hat{q}(a) = |X_q|$ . On the other hand, the assignment a sets to  $+\infty$  all monomials  $p \in F$  such that  $X_p \nsubseteq X_q$ . Each of the remaining monomials  $p \in F$  (if there is any) must satisfy  $X_p \subseteq X_q$ . But we already know that p must contain some monomial  $q' \in f_{\min}$ , that is,  $X_{q'} \subseteq X_p \subseteq X_q$ . Since both monomials q and q' are multilinear and belong to  $f_{\min}$ , this implies q = q', and hence, also  $X_p = X_q$ . Since q is multilinear and  $p \neq q$ , this means that p must have strictly larger degree |p| than  $|X_q|$ , and hence,  $\hat{p}(a) = |p| > |X_q| = \hat{f}(a)$ , a contradiction with  $\hat{F} = \hat{f}$ .

**Remark 10.** Note that Lemma 9 needs not to hold, if both polynomials are not multilinear. Say, if  $F = \min\{2x, x + y, 2y\}$  and  $f = \min\{2x, 2y\}$ , then  $\hat{F} = \hat{f}$  holds (because  $x + y \ge \min\{2x, 2y\}$ ), but  $F_{\min} = F \ne f = f_{\min}$ .

**Lemma 11.** If a circuit F computes a multilinear polynomial f over Max, then F is also multilinear, and  $F_{max} = f_{max}$ .

*Proof.* That F must also be multilinear was already shown in Lemma 7. We claim that every monomial of F must be contained in at least one monomial of f. Indeed, if some monomial  $p \in F$  is contained in none of the monomials of f, then every monomial  $q \in f$  must miss at least one variable from  $X_p$ . So, on the assignment  $a = a_p$  which sets to 1 all variables in  $X_p$ , and sets to 0 all the remaining variables, we have that  $\hat{f}(a) \leq |X_p| - 1$ . But  $\hat{F}(a) \geq \hat{p}(a) = |X_p|$ , a contradiction with  $\hat{F} = \hat{f}$ . Thus, every monomial of F must be contained in at least one monomial of f.

It remains therefore to show that  $f_{\max} \subseteq F$ . For this, assume that there is a monomial  $p \in f_{\max}$  such that  $p \notin F$ . Then, on the same assignment  $a = a_p$ , we have that  $\hat{f}(a) \geqslant \hat{p}(a) = |X_p|$ . On the other hand, every monomial  $q \in F$  such that  $X_q \not\supseteq X_p$  gets strictly smaller value  $\hat{q}(a) \leqslant |X_p| - 1$ . So, it remains to show that F cannot have any monomial  $q \ne p$  such that  $X_q \supseteq X_p$ . Indeed, we already know that every monomial  $q \in F$  must be contained in some monomial  $p' \in f_{\max}$ . Hence,  $X_q \supseteq X_p$  would imply  $X_{p'} \supseteq X_q \supseteq X_p$ . Since both monomials p' and p are multilinear and belong to  $f_{\max}$ , this would imply p' = p, and hence, also q = p since q is multilinear as well.

The following easy consequence of the structural lemmas above shows the weakness of circuits over Max, Min<sup>-</sup> and Max\* semirings: they behave like monotone arithmetic circuits.

**Corollary 12.** *Let f be a multilinear polynomial. Then:* 

- (i)  $S(f) \leq A[f]$  for every additively idempotent semiring S;
- (ii)  $S(f) = S_{lin}(f)$  for  $S \in \{Max, Min^-, Max^*, A\}$ ;
- (iii)  $Min^{-}(f) = Max^{*}(f) = A[f].$

*Proof.* Item (i) holds because in an additively idempotent semiring S (where x+x=x holds), the multiplicities of monomials have no effect on the represented function. Item (ii) follows from Lemmas 6 and 7, and the third item follows from (i) and Lemma 7.

### 5 Reduction to the Arithmetic Semiring

By Corollary 12(i), we know that  $S(f) \leq A[f]$  holds in any additively idempotent S. In particular, tropical circuits are not weaker than monotone arithmetic circuits. On the other hand, the later circuits are the easiest to analyze: they cannot produce any "redundant" monomials, those not in f. It is therefore important to know, when the converse inequality  $S(f) \geq A[f]$  holds, that is, when lower bounds on A[f] imply lower bounds on S(f). Corollary 12(iii) implies that this definitely happens in the semirings  $Min^-$  and  $Max^*$ : if f is multilinear, then

$$\operatorname{Min}^{-}(f) = \operatorname{Max}^{*}(f) = \operatorname{A}[f].$$

However, the situation with circuits over tropical semirings  $S \in \{Min, Max\}$  is completely different: here the gap between S(f) and A[f] may be even exponential. To see this, consider the st-connectivity polynomial  $f = STCON_n$ . For this polynomial, we have  $Min(f) = O(n^3)$  (see Remark 3), but it is relatively easy to show that  $A[f] = 2^{\Omega(n)}$  (see Theorem 45 below). We will now show a fact implying that the reason for such a large gap is the non-homogeneity of STCON: for homogeneous multilinear polynomials f, no gap between Min(f) and A[f] is possible at all.

Following Jerrum and Snir [15], define the *lower envelope* of a polynomial f to be the polynomial  $f_{le}$  consisting of all monomials of f of smallest degree. Similarly, the *higher envelope*,  $f_{he}$ , of f consists of all monomials of f of largest degree. Note that both polynomials  $f_{le}$  and  $f_{he}$  are homogeneous, and  $f_{le} = f_{he} = f$ , if f itself is homogeneous.

The following theorem shows that lower bounds for tropical circuits can be obtained by proving lower bounds for monotone arithmetic circuits.

**Theorem 13.** For every multilinear polynomial f, we have

$$A[f] \geqslant B_{lin}(f) \geqslant Min(f) \geqslant A[f_{le}]$$
 and  $A[f] \geqslant Max_{lin}(f) = Max(f) \geqslant A[f_{he}]$ .

If f is also homogeneous, then  $B_{lin}(f) = Min(f) = Max(f) = A[f]$ .

*Proof.* The second claim follows from the first claim and the fact that  $f_{le} = f_{he} = f$ , if f is homogeneous. So, we only have to prove the first claim. To prove that  $B_{lin}(f) \ge Min(f)$ , let F be a multilinear monotone boolean circuit computing f. Since the circuit is multilinear, its produced polynomial F is also multilinear. Since every monotone boolean function has a *unique* shortest monotone DNF, this implies that  $F_{min} = f_{min}$ . Since f and  $f_{min}$  represent the same function over Min, the circuit F with OR gates replaced by Min gates, and AND gates by Sum gates will compute f over Min.

The equality  $Max_{lin}(f) = Max(f)$  follows from Lemma 11 stating that every circuit computing a multilinear polynomial over Max must be multilinear. For the proof of the remaining inequalities  $Min(f) \ge A[f_{le}]$  and  $Max(f) \ge A[f_{he}]$ , we make use of the following simple observation.

**Claim 14.** If a polynomial F can be produced by a circuit of size s, then both  $F_{le}$  and  $F_{he}$  can be produced by homogeneous circuits of size s.

*Proof.* Take a circuit producing F. The desired homogeneous sub-circuit producing the lower or the higher envelope can be obtain by starting with input gates, and removing (if necessary) one of the wires of every sum-gate, at inputs of which polynomials of different degrees are produced.  $\Box$ 

To prove the inequality  $\operatorname{Min}(f) \geqslant \operatorname{A}[f_{\operatorname{le}}]$ , take a minimal circuit F over Min computing f. Claim 14 implies that the lower envelope  $F_{\operatorname{le}}$  of the polynomial F produced by F can be also produced by a (homogeneous) circuit of size at most  $\operatorname{Size}(F)$ . Hence,  $\operatorname{A}[F_{\operatorname{le}}] \leqslant \operatorname{Size}(F) = \operatorname{Min}(f)$ . On the other hand, Lemma 9 implies that  $f_{\operatorname{le}} = F_{\operatorname{le}}$ , and we are done. The proof of  $\operatorname{Max}(f) \geqslant \operatorname{A}[f_{\operatorname{he}}]$  is the same by using Lemma 11.

The second claim of Theorem 13 has an important implication concerning the power of dynamic programs, which can be roughly stated as follows:

For optimization problems whose target polynomials are multilinear and *homogeneous*, dynamic programming is no more powerful than monotone arithmetic circuits!

### 6 Reduction to the Boolean Semiring

A semiring S = (S, +, \*, 0, 1) is of *zero-characteristic*, if  $1 + 1 + \cdots + 1 \neq 0$  holds for any finite sum of the unity 1. Note that, with an exception of the Max semiring, all remaining semirings we consider are of zero-characteristic. If F is a circuit over a semiring S, then its *boolean version* is a monotone boolean circuit obtained by replacing every +-gate by a logical OR, and every \*-gate by a logical AND. The following seems to be a "folklore" observation.

**Lemma 15.** If F is a circuit computing a polynomial f over some semiring S, and if S is of zero-characteristic, then the boolean version of F computes f over the boolean semiring. In particular,  $S(f) \ge B(f)$ .

*Proof.* Let F be a circuit over S computing a given polynomial f. The circuit must correctly compute f on any subset of the domain S. We choose the subset  $S_+ = \{0, \overline{1}, \overline{2}, \ldots\}$ , where  $\overline{n} = 1 + \cdots + 1$  is the n-fold sum of the multiplicative unit element 1. Note that  $\overline{n} \neq 0$  holds for all  $n \geqslant 1$ , because S has zero-characteristic.

Since  $\overline{n} + \overline{m} = \overline{n+m}$  and  $\overline{n}*\overline{m} = \overline{n \cdot m}$ ,  $S_+ = (S_+, +, *, 0, 1)$  is a semiring. Since  $S_+ \subseteq S$ , the circuit must correctly compute f over this semiring as well. But the mapping  $h: S_+ \to \{0, 1\}$  given by h(0) = 0 and  $h(\overline{n}) = 1$  for all  $n \ge 1$ , is a homomorphism from  $S_+$  into the boolean semiring B with  $h(x + y) = h(x) \lor h(y)$  and  $h(x*y) = h(x) \land h(y)$ . So, the boolean version of F computes f over B.

To prove lower bounds in the boolean semiring—and hence, by Lemma 15, also in every semiring of zero characteristic—one can try to use the following general lower bounds criterion proved in [17] (see also [19, Sect. 9.4] for a simplified proof).

For  $a \in \{0,1\}$ , an *a-term* of a monotone boolean function is a subset of its variables such that, when all these variables are fixed to the constant a, the function outputs value a, independent of the values of other variables. It is easy to see that every 0-term must intersect every 1-term, and vice versa. Say that a family of sets A covers a family of sets B if every set in B contains at least one set of A.

**Definition 16.** A monotone boolean function f of n variables is t-simple if for all integers  $2 \le r, s \le n$ , such that

- (i) either the set of all 0-terms of f can be covered by  $t(r-1)^s$  s-element subsets of variables,
- (ii) or the set of all 1-terms of f can be covered by at most  $t(s-1)^r$  r-element subsets of variables plus s-1 single variables.

Note that this "asymmetry" between (i) and (ii) (allowing additional s-1 single variables in a cover) is important: say, condition (i) is trivially violated, if f contains a 0-term  $T = \{x_1, \ldots, x_k\}$  with k < s. But then (ii) is satisfied, because T must intersect all 1-terms, implying that the single variables  $x_1, \ldots, x_k$  cover all of them.

**Theorem 17** ([17]). If f is not t-simple, then B(f) > t.

**Remark 18.** One can easily show that, if the input variables can only take boolean values 0 and 1, then  $\operatorname{Min}(f) \leq 2 \cdot \operatorname{B}(f)$  holds for every multilinear polynomial. Indeed, having a (boolean) circuit F for f, just replace each AND gate  $u \wedge v$  by a Min gate  $\min(u,v)$ , and each OR gate  $u \vee v$  by  $\min(1,u+v)$ . The point however is that tropical circuits must work correctly on much larger domain than  $\{0,1\}$ . This is why lower bounds for tropical circuits do not translate to lower bounds for monotone boolean circuits. And indeed, there are explicit polynomials f, as the spanning tree polynomial  $f = \operatorname{ST}_n$ , such that  $\operatorname{B}(f) = O(n^3)$  but  $\operatorname{Min}(f) = 2^{\Omega(n)}$ ; the upper bound is shown in Remark 3, and the lower bound will be shown in Theorem 43.

**Remark 19.** When solving the so-called "bottleneck optimization" problems, one usually works in the *bottleneck* semiring  $B^* = (N \cup \{+\infty\}, \max, \min, 0, +\infty)$ . Note that over this semiring, degrees of variables are not important: say, a monomial  $p = x^2y^3$  turns to  $\min\{x, x, y, y, y\} = \min\{x, y\}$ . Thus,  $X_p = X_q$  implies  $\hat{p} = \hat{q}$ , and  $X_q \supseteq X_p$  implies that  $\hat{q} \le \hat{p}$ . Since the boolean semiring  $B = (\{0, 1\}, \max, \min, 0, 1)$  is a sub-semiring of  $B^*$ , we always have that  $B^*(f) \ge B(f)$ . (This also follows from Lemma 15, because the bottleneck semiring is of zero characteristic.) In fact, we even have an equality  $B^*(f) = B(f)$ , that is, even though the domain of  $B^*$  is much larger, the lower bounds problem for bottleneck circuits is not easier than for monotone boolean circuits.

To see this, take a circuit F computing a polynomial  $f(x_1,\ldots,x_n)$  over B, and let F be the polynomial produced by F. Since F must correctly compute f on  $\{0,1\}$ , we have that: (i) for every monomial  $q \in F$ , there must exist a monomial  $p \in f$  such that  $X_q \supseteq X_p$ , and (ii) for every monomial  $p \in f$ , there must exist a monomial  $q \in F$  such that  $X_q = X_p$ . (This can be shown by setting all variables of a monomial violating (i) or (ii) to 1, and the rest to 0; juts like in the proof of Lemma 11.) But, over the semiring  $B^*$ ,  $X_q = X_p$  implies that  $\hat{q} = \hat{p}$ , and  $X_q \supseteq X_p$  implies that  $\hat{q} \leqslant \hat{p}$ . Thus, on every input  $a \in (\mathbb{N} \cup \{+\infty\})^n$ , the maximum will be achieved on a monomial of f, implying that the circuit correctly computes f also over  $B^*$ .

### 7 Relative Power of Semirings

The reductions to the boolean and to the arithmetic semirings (Lemma 15 and Theorem 13) give us the following relations for every multilinear polynomial f:

$$B(f) \leq Min(f) \leq B_{lin}(f) \leq Min^{-}(f) = A[f]$$

and

$$B(f) \leqslant Max(f) = Max_{lin}(f) \leqslant Max^*(f) = A[f].$$

If, additionally, f is also homogeneous, then

$$B(f) \leqslant B_{lin}(f) = Min(f) = Max(f) = Min^{-}(f) = Max^{*}(f) = A[f].$$

Moreover, all inequalities are strict: for some polynomials f, one side can be even exponentially smaller than the other. Moreover, the Max/Min and Min/Max gaps can be also exponential.

To show that circuits over the tropical semirings can be exponentially weaker than those over the boolean semiring, consider the spanning tree polynomial  $f = ST_n$  and the graph connectivity polynomial  $g = CONN_n$ . Over the boolean semiring B, these polynomials represent the same boolean function: a graph is connected if and only if it has a spanning tree. This gives B(f) = B(g) and  $B_{lin}(f) = B_{lin}(g)$ . Moreover, we already know (see Remark 3) that  $B(g) = O(n^3)$  and  $Min(g) = O(n^3)$ . On the other hand, a relatively simple argument (the "rectangle bound") yields  $A[f] = 2^{\Omega(n)}$  (see Theorem 43 below). Since the polynomial f is homogeneous, Theorem 13 implies that Min(f), Max(f) and  $B_{lin}(f)$  coincide with A[f], and hence, are also exponential in n. We thus have gaps:

$$Min(f)/B(f)$$
,  $Max(f)/B(f) = 2^{\Omega(n)}$  for  $f = ST_n$ ;  
 $B_{lin}(g)/Min(g)$ ,  $B_{lin}(g)/B(g) = 2^{\Omega(n)}$  for  $g = CONN_n$ .

The latter gap  $B_{lin}(g)/B(g) = 2^{\Omega(n)}$  also shows that there is no "multilinear version" of the Floyd–Warshall algorithm, even in the boolean semiring.

To show that the remaining gaps can also be exponential, it is enough to take *any* multilinear and homogeneous polynomial  $f(x_1, \ldots, x_n)$  such that A[f] is exponential in n, and to consider its two "saturated" versions  $f_*$  and  $f^*$ , where  $f_*$  is obtained by adding to f all n monomials  $x_1, x_2, \ldots, x_n$  of degree 1, and  $f^*$  is obtained by adding to f the monomial  $x_1x_2 \cdots x_n$  of degree n.

**Lemma 20.** Let  $f(x_1,...,x_n)$  be a multilinear and homogeneous polynomial. Then both  $Min(f^*)$  and  $Max(f_*)$  are at least A[f], but all  $Max(f^*)$ ,  $Min(f_*)$  and  $B_{lin}(f_*)$  are at most n.

*Proof.* Since f is the lower envelope of  $f^*$ , and the higher envelope of  $f_*$ . Theorem 13 implies that  $Min(f^*) \ge A[f]$  and  $Max(f_*) \ge A[f]$ . On the other hand, over the Max semiring, the polynomial  $f^*$  computes  $x_1 + x_2 + \cdots + x_n$ , whereas over the Min semiring,  $f_*$  computes  $min\{x_1, x_2, \ldots, x_n\}$ , and computes  $x_1 \lor x_2 \lor \cdots \lor x_n$  over the boolean semiring. Hence, all  $Max(f^*)$ ,  $Min(f_*)$  and  $B_{lin}(f_*)$  are at most n.

Since, there are many linear and homogeneous polynomials requiring monotone arithmetic circuits of exponential size (see, e.g. Table 1), the saturated versions of f immediately give exponential gaps.

Still, the "saturation trick" leads to somewhat artificial examples, and it would be interesting to establish exponential gaps using "natural" polynomials. For example, the Max/Min gap is achieved already on a very natural st-connectivity polynomial  $h = STCON_n$ . We know that  $Min(h) = O(n^3)$  (Remark 3), but a simple argument (see Theorem 45) shows that  $Max(h) = 2^{\Omega(n)}$ . Hence,

$$Max(h)/Min(h) = 2^{\Omega(n)}$$
 for  $h = STCON_n$ .

From now on we concentrate on the lower bound *arguments* themselves.

### 8 Lower Bounds for Separated Polynomials

Let  $g(x_1,...,x_n)$  be a polynomial in  $n \ge 3$  variables. An *enrichment* of g is a polynomial h in n-1 variables obtained by taking some variable  $x_k$  and replacing it by a sum  $x_i + x_j$  or by a product  $x_i x_j$  of some other two (not necessarily distinct) variables, where  $k \notin \{i, j\}$ . A *progress measure* of polynomials is an assignment of non-negative numbers  $\mu(g)$  to polynomials g such that

- (i)  $\mu(x_i) = 0$  for each variable  $x_i$ ;
- (ii)  $\mu(h) \leq \mu(g) + 1$  for every enrichment h of g.

**Lemma 21.** For every polynomial f, and every progress measure  $\mu(f)$ , we have  $A[f] \geqslant \mu(f)$ .

*Proof.* Take a monotone arithmetic circuit F with s = A[f] gates producing f. We argue by induction on s. If s = 0, then  $F = x_i$  is an input variable, and we have  $A[f] = 0 = \mu(f)$ . For the induction step, take one of the first gates  $u = x_i \circ x_j$  of F, where  $o \in \{+,\cdot\}$ . Let  $F'(x_1,\ldots,x_n,y)$  be the circuit with the gate u replaced by a new variable y. Hence, Size(F') = Size(F) - 1 and  $F(x_1,\ldots,x_n)$  is an enrichment of  $F'(x_1,\ldots,x_n,y)$ . By the induction hypothesis, we have that  $Size(F') \geqslant \mu(F')$ . Together with  $\mu(F) \leqslant \mu(F') + 1$ , this yields  $Size(F) = Size(F') + 1 \geqslant \mu(F') + 1 \geqslant \mu(F)$ .  $\square$ 

Recall that a monomial p contains a monomial q (as a factor), if p = qq' for some monomial q'.

**Definition 22.** A sub-polynomial  $P \subseteq f$  is *separated* if the product pq of any two monomials p and q of P contains no monomial of f distinct from p and q. Let

$$sep(f) := max\{|P| - 1 : P \subseteq f \text{ is separated}\}.$$

Note that we consider separateness within the entire set f of monomials: it is not enough that the product pq contains no third monomial of P—it must not contain any third monomial of the entire polynomial f.

Note also that a multilinear polynomial f of minimum degree m is separated, if every monomial of f is uniquely determined by any subset of  $\lceil m/2 \rceil$  its variables. (Being uniquely determined means that no other monomial contains the same subset of variables.) Indeed, if p\*q contains some monomial r then r and p (or r and q) must share at least  $\lceil m/2 \rceil$  variables, implying that r = p (or r = q) must hold.

**Example 23.** A standard construction of separated polynomials with many monomials is the following. Let  $n = q^2$  where q is a prime number. The polynomial  $f_{n,k}$  has  $n^2$  variables  $x_{i,j}$  with  $i,j \in GF(q)$ , and is defined by:

$$f_{n,k}(x) = \sum_{\pi} \prod_{i \in GF(q)} x_{i,\pi(i)},$$

where the sum is taken over all (one-variable) polynomials  $\pi(z)$  of degree at most k-1 over GF(q). The polynomial has  $q^k = n^{k/2}$  monomials, and each of them is determined by any subset of k variables (since no polynomials of degree  $\leqslant k-1$  can have k or more roots). Thus, for every  $k \leqslant \lfloor q/2 \rfloor$ , the polynomial  $f_{n,k}$  is separated, and the following theorem implies that  $A[f_{n,k}] \geqslant n^{k/2} - 1$ . Since the polynomial is linear and homogeneous, the same bound holds for tropical circuits, as well. This is almost tight, because clearly  $A[f_{n,k}] \leqslant q^{k+1} = n^{k/2+1}$ .

**Theorem 24** (Schnorr [38]). For every polynomial f, we have  $A[f] \ge sep(f)$ , where

$$sep(f) := max\{|P| - 1 : P \subseteq f \text{ is separated}\}.$$

In particular,  $A[f] \ge |f| - 1$  if the polynomial f itself is separated.

*Proof.* It is enough to show that the measure sep(f) is a progress measure. The first condition (i) is clearly fulfilled, since  $sep(x_i) = 1 - 1 = 0$ . To verify the second condition (ii), let  $f(x_1, \ldots, x_n, y)$  be a polynomial, and  $h(x_1, \ldots, x_n)$  be its enrichment. Our goal is to show that  $sep(f) \ge sep(h) - 1$ . We only consider the "hard" case when y is replaced by a sum of variables:  $h(x_1, \ldots, x_n) = f(x_1, \ldots, x_n, u + v)$ , where  $u, v \in \{x_1, \ldots, x_n\}$ .

To present the proof idea, we first consider the case when no monomial of f contains more than one occurrence of the variable y. Then every monomial yp of f turns into two monomials up and vp of h. To visualize the situation, we may consider the bipartite graph  $G \subseteq f \times h$ , where every monomial  $yp \in f$  is connected to two monomials  $up, vp \in h$ ; each monomial  $q \in f$  without  $q \in h$ . Take now a separated subset  $q \in h$  such that |P| - 1 = sep(h), and let  $q \in h$  be the set of its neighbors in  $q \in h$ . Our goal is to show that:

- (a)  $|Q| \ge |P| 1$ , and
- **(b)** *Q* is separated.

Then the desired inequality  $sep(f) \ge |Q| - 1 \ge |P| - 2 = sep(h) - 1$  follows.

To show item (a), it is enough to show that at most one monomial in Q can have both its neighbors in P. To show this, assume that this holds for some two monomials yp and yq of Q. Then all four monomials up, vp, uq, vq belong to P. But this contradicts the separateness of P, because the product up\*vq contains the third monomial uq (and vp).

To show item (b), assume that the product p\*q of some two monomials  $p \neq q$  of Q contains some third monomial  $r \in h$ . Let  $p', q' \in P$  be some neighbors of p and q lying in P. Then the product p'\*q' must contain one (of the two) neighbors of p. Since p of these neighbors of p belong to p, we obtain a contradiction with the separateness of p.

In general (if y can have any degrees in f), a monomial  $y^kp$  of f has k+1 neighbors  $u^iv^{k-i}p$ ,  $i=0,1,\ldots,k$  in h. To show (a), it is again enough to show that at most one monomial in Q can have two neighbors in P. For this, assume that there are two monomials  $p \neq q$  such that all four monomials  $u^av^{k-a}p, u^bv^{k-b}p, u^cv^{l-c}q, u^dv^{l-d}q$  belong to P. Assume w.l.o.g. that  $a=\max\{a,b,c,d\}$ . Then the product of  $u^av^{k-a}p$  and  $u^cv^{l-c}q$  contains  $u^av^{l-c}q$ , and (since  $c \leq a$ ) contains the monomial  $u^av^{l-a}q$  of h, contradicting the separateness of P. The proof of (b) is similar.

**Remark 25.** It is not difficult to see that we have a stronger inequality  $sep(f) \ge sep(h)$ , if the variable y is replaced by the product uv (instead of the sum u + v). Thus, in fact, Theorem 24 gives a lower bound on the number of sum gates.

As a simple application of Schnorr's argument, consider the triangle polynomial

$$TR_n(x, y, z) = \sum_{i,j,k \in [n]} x_{ik} y_{kj} z_{ij}.$$

This polynomial has 3n variables and  $n^3$  monomials.

**Corollary 26.** If  $f = TR_n$ , then  $Min(f) = Max(f) = A[f] = \Theta(n^3)$ .

*Proof.* The equalities Min(f) = Max(f) = A[f] hold by Theorem 13, because f is multilinear and homogeneous. The upper bound  $A[f] = O(n^3)$  is trivial. To prove the lower bound  $A[f] = \Omega(n^3)$ , observe that every monomial  $p = x_{ik} y_{kj} z_{ij}$  of f is uniquely determined by any choice of any two of its three variables. This implies that p cannot be contained in a union of any two monomials distinct from p. Thus, the polynomial f is separated, and its Schnorr's measure is  $sep(f) = n^3 - 1$ . Theorem 24 yields  $A[f] \ge sep(f) = n^3 - 1$ , as desired.

Recall that the k-clique polynomial  $\text{CLIQUE}_{n,k}$  has  $\binom{n}{k}$  monomials  $\prod_{i < j \in S} x_{ij}$  corresponding to subsets  $S \subseteq [n]$  of size |S| = k. This is a homogeneous multilinear polynomial of degree  $\binom{k}{2}$ . Note that  $\text{TR}_n$  is a sub-polynomial of  $\text{CLIQUE}_{3n,3}$  obtained by setting some variables to 0.

By Lemma 15, an exponential lower bound for  $CLIQUE_{n,s}$  over the tropical Min follows from Razborov's lower bound for this polynomial over the boolean semiring B [37]. However, the proof over B is rather involved. On the other hand, in tropical semirings such a bound comes quite easily.

**Corollary 27.** For 
$$f = \text{CLIQUE}_{n,k}$$
,  $\text{Min}(f)$ ,  $\text{Max}(f)$  and  $\text{B}_{\text{lin}}(f)$  all are at least  $\binom{n}{k} - 1$ .

This lower bound on  $B_{lin}(f)$  was proved by Krieger [24] using different arguments.

*Proof.* Since f is multilinear and homogeneous, it is enough (by Theorems 13) to show the corresponding lower bound on A[f]. By Theorem 24, it is enough to show that f is separated.

Assume for the sake of contradiction, that the union of two distinct k-cliques A and B contains all edges of some third clique C. Since all three cliques are distinct and have the same number of nodes, C must contain a node u which does not belong to A and a node v which does not belong to B. This already leads to a contradiction because either the node u (if u = v) or the edge  $\{u, v\}$  (if  $u \neq v$ ) of C would remain uncovered by the cliques A and B.

Recall that the dynamic programming algorithm of Floyd–Warshall implies that the all-pairs shortest path polynomial APSP<sub>n</sub>, and hence, also the matrix product polynomial MP<sub>n</sub>, have Min-circuits of size  $O(n^3)$ ; see Theorem 2. On the other hand, using Theorem 24 one can show that this algorithm is optimal: a cubic number of gates is also necessary.

**Corollary 28.** Both  $Min(APSP_n)$  and  $Min(MP_n)$  are  $\Theta(n^3)$ .

*Proof.* It is enough to show that  $Min(MP_n) = \Omega(n^3)$ . Recall that  $MP_n(x,y)$  is the set of all  $n^2$  polynomials  $f_{ij} = \sum_{k \in [n]} x_{ik} y_{kj}$ . Since the triangle polynomial  $TR_n = \sum_{i,j \in [n]} z_{ij} f_{ij}$  is just a single-output version of  $MP_n$ , and its complexity is by at most an additive factor of  $2n^2$  larger than that of  $MP_n$ , the desired lower bound for  $MP_n$  follows directly from Corollary 26.

Kerr [23] earlier proved  $Min(MP_n) = \Omega(n^3)$  using a different argument, which essentially employs the fact the Min semiring contains more than two distinct elements. Since this "domain-dependent" argument may be of independent interest, we sketch it.

*Proof.* (Due to Kerr [23]) Let F be a Min-circuit computing all  $n^2$  polynomials

$$f_{ii}(x) = \min\{x_{ik} + y_{ki} : k = 1, ..., n\}.$$

By Lemma 9, for each polynomial  $f_{ij}$  there must be a gate  $u_{ij}$ , the polynomial  $F_{ij}$  produced at which is of the form  $F_{ij} = \min\{f_{ij}, G_{ij}\}$ , where  $G_{ij}$  is some set of monomials (sums), each containing at least one monomial of  $f_{ij}$ .

Assign to every monomial  $p = x_{ik} + y_{kj}$  of  $f_{ij}$  a sum gate  $u_p$  with the following two properties: (i) p is produced at  $u_p$ , and (ii) there is a path from  $u_p$  to  $u_{ij}$  containing no sum gates. Since a + a = a does not hold in Min, at least one such gate must exist for each of the monomials  $x_{ik} + y_{kj}$ .

It remains therefore to show that no other term  $x_{ab} + y_{bc}$  can get the same gate  $u_p$ . To show this, assume the opposite. Then at the gate  $u_p$  some sum

$$\min\{x_{ik},\alpha,\ldots\}+\min\{y_{kj},\ldots\}$$

is computed, where  $\alpha \in \{x_{ab}, y_{bc}\}$  is a single variable distinct from  $x_{ik}$  and  $y_{kj}$ . Set  $\alpha := 0$ ,  $x_{ik} = y_{kj} := 1$  and set all remaining variables to 2. Then the first minimum in the sum above evaluates to 0, and we obtain  $\hat{F}_{ij}(x) \leq 1$ . But  $\hat{f}_{ij}(x) = 2$  because the term  $x_{ik} + y_{kj}$  gets value 1 + 1 = 2, and the remaining terms of  $f_{ij}$  get values  $\geq 2 + 0 = 2$ . This gives the desired contradiction.

**Remark 29.** Using more subtle arguments, Paterson [33], and Mehlhorn and Galil [29] succeeded to prove a cubic lower bound  $\Omega(n^3)$  for MP<sub>n</sub> even over the boolean semiring B.

We finish this section by mentioning one important result showing that circuits producing a single polynomial f must, in fact, also produce all partial derivatives of f.

The (formal) partial derivative  $\partial f/\partial x_i$  of a polynomial is the sum of partial derivatives  $\partial p/\partial x_i$  of its monomials  $p=x_i^aq$ , where  $\partial p/\partial x_i=0$  if a=0 ( $x_i$  does not appear in p), and  $\partial p/\partial x_i=ax_i^{a-1}q$  if  $a\geqslant 1$ . In particular, if f is multilinear, then  $\partial f/\partial x_i$  is a polynomial obtained from f by removing all monomials not containing  $x_i$ , and removing  $x_i$  from all the remaining monomials. The *gradient* of f is the vector  $\nabla f=(\partial f/\partial x_1,\ldots,\partial f/\partial x_n)$ .

**Theorem 30** (Baur and Strassen [5]). For every polynomial f,  $A[f, \nabla f] \leq 4 \cdot A[f]$ .

This result holds not only for monotone but also for non-monotone arithmetic circuits, where subtractions are allowed. Morgenstern [31] has shown that a slightly worse upper bound (with constant 4 replaced by 5) can be proved by an easy induction using the chain rule for partial derivatives. Some extensions and improvements of Theorem 30 were given by Gashkov and Gashkov [11], and Sergeev [39]. Instead of the chain rule, they use the (simpler) product rule together with the so-called "transposition principle" for linear circuits (those not using product gates).

If the polynomial f is multilinear and homogeneous, then Theorems 30 and 13 imply that  $S(f, \nabla f) \le 4 \cdot S(f)$  hods for every tropical semiring f. Since the gradient of the triangle polynomial f is  $\sum_{i,j\in[n]} z_{ij} f_{ij}$  contains all f polynomials f if f is f if f is f in particularly have that f if f is f in f i

### 9 Decompositions and Cuts

Besides the gate-elimination method, most of lower bound arguments for monotone arithmetic circuits follow the following general frame: if a polynomial f can be produced by a circuit of size s, then f can be written as a sum  $f = \sum_{i=1}^{t} g_i$  of t = O(s) "rectangles"  $g_i$ . Usually, these "rectangles"  $g_i$  are products of two (or more) polynomials of particular degrees. Let us first explain, where these "rectangles" come from.

Let F be a circuit over some semiring S = (S, +, \*, 0, 1). For a gate u in F, let  $f_u$  denote the polynomial produced at u, and let  $F_{u=0}$  denote the circuit obtained from F by replacing the gate u by the additive identity 0. We now assume that a\*0 = 0 holds for all  $a \in S$ . Hence, the polynomial  $F_{u=0}$  produced by  $F_{u=0}$  consists of only those monomials of F which do not "use" the gate u for their production. To avoid trivialities, we will always assume that  $F_{u=0} \neq F$ , i.e. that there are no "redundant" gates.

**Lemma 31.** For every gate u in F, the polynomial F produced by F can be written as a sum  $F = F_u + F_{u=0}$  of two polynomials, the first of which has the form  $F_u = f_u * \overline{f}_u$  for some polynomial  $\overline{f}_u$ .

*Proof.* If we replace the gate u by a new variable y, the resulting circuit produces a polynomial of the form  $y*A + F_{u=0}$  for some polynomial A. It remains to substitute all occurrences of the variable y with the polynomial  $f_u$  produced at the gate u.

When analyzing circuits, the following concept of "parse graphs" is often useful. A *parse-graph* G in F is defined inductively as follows: G includes the root (output gate) of F. If u is a sum-gate, then exactly one of its inputs is included in G. If u is a product gate, then both its input gates are included in G. Note that each parse-graph produces exactly one monomial in a natural way, and that each monomial  $p \in F$  is produced by at least one parse-graph. If p is multilinear, then each parse-graph for p is a tree.

**Remark 32.** Roughly speaking, the number  $|F_u|$  of monomials in the polynomial  $F_u$  is the "contribution" of the gate u to the production of the entire polynomial F. Intuitively, if this contribution is small for many gates, then there must be many gates in F. More formally, associate with each monomial  $p \in F$  some of its parse-graphs  $F_p$  in F. Observe that  $u \in F_p$  implies  $p \in F_u$ . Thus, double-counting yields

$$\operatorname{Size}(\mathsf{F}) = \sum_{u \in \mathsf{F}} 1 \geqslant \sum_{u \in \mathsf{F}} \sum_{p \in F \colon u \in \mathsf{F}_p} \frac{1}{|F_u|} = \sum_{p \in F} \sum_{u \in \mathsf{F}_p} \frac{1}{|F_u|} \geqslant |F| \cdot \min_{p \in F} \sum_{u \in \mathsf{F}_p} \frac{1}{|F_u|}.$$

So, in principle, one can obtain strong lower bounds on the total number of gates in F by showing that this latter minimum cannot be too small.

The polynomial  $\overline{f}_u$  in Lemma 31 can be explicitly described by associating polynomials with paths in the circuit F. Let  $\pi$  be a path from a gate u to the output gate,  $u_1, \ldots, u_m$  be all product gates along this path (excluding the first gate u, if it itself is a product gate), and  $w_1, \ldots, w_m$  be input gates to these product gates not lying on the path  $\pi$ . We associate with  $\pi$  the polynomial  $f_{\pi} := f_{w_1} * f_{w_2} \cdots f_{w_m}$ . Then

$$\overline{f}_u = \sum_{\pi} f_{\pi} \,,$$

where the sum is over all paths  $\pi$  from u to the output gate.

Lemma 31 associates sub-polynomials  $f_u * \overline{f}_u$  of F with *nodes* (gates) u of F. In some situations, it is more convenient to associate sub-polynomials with *edges*. For this, associate with every edge e = (u, v), where  $v = u \circ w$  is some gate with  $o \in \{+, *\}$  of F, the polynomial

$$\overline{f}_e := A * \overline{f}_v$$
 where  $A = \begin{cases} 1 & \text{if } \circ = +; \\ f_w & \text{if } \circ = *. \end{cases}$ 

That is,  $\overline{f}_e = \overline{f}_v$  if v is a sum gate, and  $\overline{f}_e = f_w * \overline{f}_v$  if v is a product gate.

A *node-cut* in a circuit is a set U of its nodes (gates) such that every input-output path contains a node in U. Similarly, an *edge-cut* is a set E of edges such that every input-output path contains an edge in E. Recall that, in our notation, "f = h" for two polynomials f and h only means that their *sets* of monomials are the same—their multiplicities (coefficients) may differ.

**Lemma 33.** If U is a node-cut and E an edge-cut in a circuit F, then

$$F = \sum_{u \in U} f_u * \overline{f}_u = \sum_{e = (u, v) \in E} f_u * \overline{f}_e.$$

*Proof.* The fact that all monomials of the last two polynomials are also monomials of F follows from their definitions. So, it is enough to show that every monomial  $p \in F$  belongs to both of these polynomials. For this, take a parse graph  $F_p$  of p. Since U forms a node-cut, the graph  $F_p$  must contain some node  $u \in U$ . The monomial p has a form  $p = \underline{p'p''}$  where p' is the monomial produced by the subgraph of  $F_p$  rooted in u. Hence,  $p' \in f_u$  and  $p'' \in \overline{f_u}$ . Similarly, since E forms an edge-cut, the graph  $F_p$  contains some edge  $e = (u, v) \in E$ . The monomial p has the form  $p = \underline{p'p''}$  where p' is the monomial produced by the subgraph of  $F_p$  rooted in p. Hence,  $p' \in f_u$  and  $p'' \in \overline{f_e}$ .

### 10 Lower Bounds for (k, l)-free Polynomials

A polynomial f is (k,l)-free  $(1 \le k \le l)$  if f does not contain a product of two polynomials, one with > k monomials and the other with > l monomials. A polynomial f is f-free if it is (k,k)-free, that is, if

$$A*B \subseteq f \text{ implies min}\{|A|,|B|\} \leqslant k.$$

Note that this alone gives no upper bound on the total number |A\*B| of monomials in the product A\*B.

**Theorem 34.** If a (k,l)-free polynomial f can be produced by a circuit of size s, then f can be written as a sum of at most 2s products  $A \times B$  with  $|A| \le k$  and  $|B| \le l^2$ . In particular,

$$A[f] \geqslant \frac{|f|}{2kl^2}.$$

*Proof.* Our argument is a mix of ideas of Gashkov and Sergeev [12], and of Pippenger [34]. Take a minimal circuit F producing f; hence, F = f is (k,l)-free. This implies that every product gate u = v\*w in F must have an input, say w, at which a "small" set  $A = f_w$  of only  $|A| \le l$  monomials is produced. We thus can remove the edge (w,u) and replace u by a unary (fanin-1) gate u = v\*A of scalar multiplication by this fixed (small) polynomial A. If both inputs produce small polynomials, then we eliminate only one of them. What we achieve by doing this is that input gates remain the same as in the original circuit (variables  $x_1, \ldots, x_n$  and constants 0, 1), each product gate has fanin 1, and for every edge e = (u, v) in the resulting circuit F', we have an upper bound

$$|\overline{f}_e| \leqslant l \cdot |\overline{f}_v| \,. \tag{1}$$

Say that an edge e = (u, v) in F' is legal if both  $|f_u| \le k$  and  $|\overline{f_e}| \le l^2$  hold. Let E be the set of all legal edges; hence, Size(F)  $\ge |E|/2$ . By Lemma 33, it remains to show that E forms an edge-cut of F'.

To show this, take an arbitrary input-output path P in F', and let e = (u, v) be the last edge of P with  $|f_u| \le k$ . If v is the output gate, then  $\overline{f}_v$  is a trivial polynomial 1, and hence,  $|\overline{f}_e| \le l$  by

(1), meaning that e is a legal edge. Suppose now that v is not the output gate. Then  $|f_u| \leq k$  but  $|f_v| > k$ . Held also  $|\overline{f}_e| > l^2$ , then (1) would imply that  $|\overline{f}_v| \geq |\overline{f}_e|/l > l$ . Together with  $|f_v| > k$  and  $f_v * \overline{f}_v \subseteq F$ , this would contradict the (k,l)-freeness of F. Thus,  $|f_u| \leq k$  and  $|\overline{f}_e| \leq l^2$ , meaning that e = (u,v) is a legal edge.

Together with Theorem 13, Theorem 34 yields the following lower bound over tropical semirings for polynomials, whose only lower or higher envelopes are required to be (k,l)-free.

**Corollary 35.** Let f and g be polynomials such that  $f_{le}$  and  $g_{he}$  are (k,l)-free for some  $1 \le k \le l$ . Then

 $Min(f) \geqslant \frac{|f_{le}|}{2kl^2}$  and  $Max(g) \geqslant \frac{|g_{he}|}{2kl^2}$ .

**Remark 36.** Using a deeper analysis of circuit structure, Gashkov and Sergeev [10, 12] were able to even estimate the numbers of sum and product gates: every monotone arithmetic circuit computing a (k,l)-free polynomial f of n variables must have at least |f|/K-1 sum gates, and at least  $2\sqrt{|f|/K}-n-2$  product gates, where  $K=\max\{k^3,l^2\}$ .

**Remark 37.** Every boolean  $n \times n$  matrix  $A = (a_{ij})$  defines a set  $Ay = (f_1, \ldots, f_n)$  of n linear polynomials  $f_i(y) = \sum_j a_{ij} y_j$ , as well as a single-output bilinear polynomial  $f_A(x,y) = \sum_i x_i f_i(y) = \sum_{i,j: a_{ij}=1} x_i y_j$  on 2n variables. Call a boolean matrix A(k,l)-free, if it does not contain any (k+1,l+1) all-1 submatrix. It is clear that the polynomial  $f_A$  is (k,l)-free if and only if the matrix A is (k,l)-free.

Results of Nechiporuk [32] (re-discovered later by Mehlhorn [28] and Pippenger [34]) imply that, if A is (k,k)-free, then  $B(Ax) \ge |A|/4k^3$ , where |A| is the number of 1-entries in A. This, however, does not immediately yield a similar lower bound on  $B(f_A)$  for the *single-output* version  $f_A$  and, in fact, no such bound is known so far in the boolean semiring. (A lower bound  $B(f_A) \ge |A|$  for (1,1)-free matrices is only known when restricted to circuits with gates of fanout 1, i.e., to formulas; see [19, Theorem 7.2].) On the other hand, Theorem 34 gives such a bound at least for tropical and multilinear boolean circuits: if A is (k,k)-free, then

$$Min(f_A) = Max(f_A) = B_{lin}(f_A) = A[f_A] \ge |A|/2k^3$$
,

where the equalities follow from Theorem 13, because the polynomial  $f_A$  is homogeneous.

### 11 Rectangle Bound

For a polynomial f, let d(f) denote its minimum degree, i.e., the minimum degree of a monomial of f. Various versions of the following fact were observed by several authors including Hyafil [14], Jerrum and Snir [15] and Valiant [43].

**Lemma 38** (Sum-of-Products). If a polynomial f of minimum degree at least  $m \ge 3$  can be produced by a circuit of size s, then f can be written as a sum  $f = \sum_{i=1}^{t} g_i * h_i$  of  $t \le s$  products  $g_i * h_i$  of polynomials such that  $m/3 < d(g_i) \le 2m/3$ .

*Proof.* Let d = d(f) be the minimum degree of f, and let F be a circuit of size s producing f; hence, F = f and  $d \ge m$ . By the degree  $d_u$  of a gate  $u \in F$  we will mean the minimum degree of the polynomial produced at u. In particular, the degree of the output gate is d.

**Claim 39.** For every  $\epsilon \in (1/d, 1)$ , there exists a gate u with  $\epsilon d/2 < d_u \le \epsilon d$ .

*Proof.* Start at the output gate of F, and traverse the circuit (in the reverse order of edges) by always choosing the input of larger degree until a gate v of degree  $d_v > \epsilon d$  is found such that both its inputs u and w have degrees at most  $\epsilon d$ . Assume w.l.o.g. that  $d_u \geqslant d_w$ . Since  $d_v \leqslant d_u + d_w \leqslant 2d_u$ , the gate u has the desired degree  $\epsilon d/2 < d_u \leqslant \epsilon d$ .

Now, we apply Claim 39 with  $\epsilon := 2m/3d$  to find a gate u of degree  $m/3 = \epsilon d/2 < d_u \le \epsilon d = 2m/3$ . By Lemma 31, we can write F as  $F = f_u * \overline{f}_u + F_{u=0}$  where  $f_u$  is the polynomial produced at u. Since  $d(f_u) = d_u$ , we have that  $m/3 < d(f_u) \le 2m/3$ . The polynomial  $F_{u=0}$  is obtained from F by removing some monomials. If  $F_{u=0}$  is empty, then we are done. Otherwise, the polynomial  $F_{u=0}$  still has minimum degree at least m, and can be produced by a circuit with one gate fewer. So, we can repeat the same argument for the polynomial  $F_{u=0}$ , until the empty polynomial is obtained.  $\square$ 

**Remark 40.** Lemma 38 remains true if, instead of the minimum degree measure d(f) of polynomials, one takes the minimum length l(f) of a monomial of f, where the *length* of a monomial p is defined as the number  $|X_p|$  of distinct variables occurring in p. The same argument works because  $l(F_{u=0}) \ge l(F)$ , as long as the polynomial  $F_{u=0}$  is not empty.

To upper-bound the maximal possible number |A\*B| of monomials in a product-polynomial  $A*B \subseteq f$ , the following measure of *factor-density* naturally arises: for an integer  $r \ge 0$ , let  $\#_r(f)$  be the maximum number of monomials in f containing a fixed monomial of degree f as a common factor. This measure tells us how much the monomials of f are "stretched": the faster  $\#_r(f)$  decreases with increasing f, the more stretched f is. Note that, if f is the maximum degree of f, then

$$1 = \#_d(f) \leqslant \#_{d-1}(f) \leqslant \ldots \leqslant \#_1(f) \leqslant \#_0(f) = |f|.$$

**Observation 41.** Let A and B be polynomials of maximum degrees a and b. If  $A*B \subseteq f$ , then  $|A*B| \leq \#_a(f) \cdot \#_b(f)$ .

*Proof.* Fix a monomial  $p \in A$  of degree |p| = a, and a monomial  $q \in B$  of degree |q| = b. Since  $\{p\}*B \subseteq f$ , we have that  $|B| = |\{p\}*B| \le \#_{|p|}(f) = \#_a(f)$ . Similarly, since  $A*\{q\} \subseteq f$ , we have that  $|A| = |A*\{q\}| \le \#_{|q|}(f) = \#_b(f)$ .

**Lemma 42** (Rectangle Bound). For every polynomial f of minimum degree at least  $m \ge 3$ , there is an integer  $m/3 < r \le 2m/3$  such that

$$A[f] \geqslant \frac{|f|}{\#_r(f) \cdot \#_{m-r}(f)}.$$

*Proof.* Let F be a minimal monotone arithmetic circuit representing f, and let s = Size(F). By Lemma 38, the polynomial F = f can be written as a sum of at most s products A\*B of polynomials, where the minimum degree a = d(A) of A satisfies  $m/3 \le a \le 2m/3$ ; hence,  $d(B) \ge m - a$ . Observation 41 implies that  $|A*B| \le \#_a(f) \cdot \#_{m-a}(f)$ .

The Rectangle Bound allows one to easily obtain strong lower bounds for some explicit polynomials.

**Theorem 43.** If  $f \in \{PER_n, HC_n, ST_n\}$ , then Min(f), Max(f) and  $B_{lin}(f)$  all are  $2^{\Omega(n)}$ .

*Proof.* Since all these three polynomials f are multilinear and homogeneous, it is enough (by Theorem 13) to prove the corresponding lower bounds on A[f]. We will obtain such bounds by applying Lemma 42.

The permanent polynomial  $f = \text{PER}_n$  has |f| = n! multilinear monomials  $x_{1,\pi(1)}x_{2,\pi(2)}\cdots x_{n,\pi(n)}$ , one for each permutation  $\pi: [n] \to [n]$ . Since at most (n-r)! of the permutations can take r predescribed values, we have that  $\#_r(f) \le (n-r)!$ . Lemma 42 gives  $A[f] \ge n!/(n-r)!r! = \binom{n}{r}$  for some  $n/3 < r \le 2n/3$ ; so,  $A[f] = 2^{\Omega(n)}$ .

The argument for  $HC_n$  is almost the same: the only difference is that now the monomials correspond to symmetric, not to all permutations.

The spanning tree polynomial  $f = \operatorname{ST}_n$  is a homogeneous polynomial of degree n-1 with  $|f| = n^{n-2}$  monomials  $x_{2,\pi(2)}x_{3,\pi(3)}\cdots x_{n,\pi(n)}$  corresponding to the functions  $\pi:\{2,3,\ldots,n\}\to [n]$  such that  $\forall i\ \exists k\colon \pi^{(k)}(i)=1$ . Each spanning tree gives a function with this property by mapping sons to their father. Now, if we fix some r edges, then r values of functions  $\pi$  whose spanning trees contain these edges are fixed. Thus,  $\#_r(f)\leqslant (n-r)^{n-r-2}$ , and Lemma 42 gives  $\operatorname{A}[f]=2^{\Omega(n)}$ .

**Remark 44.** Fomin et al. [8] have shown that the spanning tree polynomial  $ST_n$  can be computed by a monotone arithmetic circuit of size  $O(n^3)$ , if divisions are allowed. The analogue of division x/y in tropical circuits is subtraction x-y. Thus, the result implies that, over the tropical semifield (R, max, +, -), the polynomial  $ST_n$  can be computed by a circuit of size  $O(n^3)$ . This extends to tropical circuits the result of Valiant [43] stating that subtractions may be exponentially powerful in arithmetic circuits.

The three polynomials in Theorem 43 are homogeneous. To show that the rectangle bound works also for non-homogeneous polynomials, consider the st-connectivity polynomial STCON<sub>n</sub>. We know that this polynomial has Min-circuits of size  $O(n^3)$  (Remark 3). But Max-circuits for this polynomial must be of exponential size.

**Theorem 45.** If  $f = STCON_{n+2}$ , then Max(f) and  $Min^{-}(f)$  are at least  $2^{\Omega(n)}$ .

*Proof.* Consider the higher envelope  $f_{he}$  of f. This is a homogeneous polynomial of degree n with  $|f_{he}| = n!$  monomials corresponding to paths in  $K_{n+2}$  from s = 0 to t = n+1 with exactly n inner nodes. Since  $\#_r(f) \leq (n-r)!$ , Lemma 42 (with r = n/3) gives  $A[f_{he}] = 2^{\Omega(n)}$ . By Theorem 13, the same lower bound holds for Max(f) and  $Min^-(f)$ .

### 12 Truly Exponential Lower Bounds

Note that the lower bounds above have the forms  $2^{\Omega(\sqrt{n})}$ , where n is the number of variables. Truly exponential lower bounds  $A[f] = \Omega(2^{n/2})$  on the monotone circuit size of multilinear polynomials of n variables were announced by Kasim-Zade [21, 22]. Somewhat earlier, a lower bound  $A[f] = 2^{\Omega(n)}$  was announced by Kuznetsov [25]. Then, Gashkov [10] proposed a general lower bounds argument for monotone arithmetic circuits and used it to prove an  $A[f] = \Omega(2^{2n/3})$  lower bound.

The construction of the corresponding multilinear polynomials in these works is algebraic. Say, the monomials of the polynomial f(x,y) of 2n variables constructed in [21, 22] have the form  $x_1^{a_1} \cdots x_n^{a_n} y_1^{b_1} \cdots y_n^{b_n}$  where  $a \in GF(2)^n$  and  $b = a^3$  (we view vector a as an element of  $GF(2^n)$  when rising it to the 3rd power). That is, monomials correspond to the points of the cubic parabola  $\{(a,a^3): a \in GF(2^n)\}$ . The monomials of the polynomial constructed in [10] are defined using triples (a,b,c) with  $a,b,c \in GF(2^n)$  satisfying  $a^3 + b^7 + c^{15} = 1$ . The constructed polynomials are (k,l)-free for particular *constants* k and l, and the desired lower bounds follow from general lower bounds

of Gashkov [10], and Gashkov and Sergeev [12] for (k,l)-free polynomials (see Sect. 10 for these bounds).

Without knowing these results, Raz and Yehudayoff [35] have recently used discrepancy arguments and exponential sum estimates to derive a truly exponential lower bound  $A[f] = 2^{\Omega(n)}$  for an explicit multilinear polynomial  $f(x_1, \ldots, x_n)$ . Roughly, their construction of f is as follows. Assume that n divided by a particular constant k is a prime number. View a monomial p as a 0/1 vector of its exponents. Split this vector into k blocks of length n/k, view each block as a field element, multiply these elements, and let  $c_p \in \{0,1\}$  be the first bit of this product. Then include the monomial p in f if and only if  $c_p = 1$ .

In this section we use some ideas from [18] to show that truly exponential lower bounds can be also proved using graphs with good expansion properties. Numerically, our bounds (like those in [35]) are worse than the bounds in [21, 22, 10, 12] (have smaller constants), but the construction of polynomials is quite simple (modulo the construction of expander graphs).

Say that a partition  $[n] = S \cup T$  is balanced if  $n/3 \le |S| \le 2n/3$ . Define the matching number m(G) of a graph G = ([n], E) as the largest number m such that, for every balanced partition of nodes of G, at least m crossing edges form an induced matching. An edge is crossing if it joins a node in one part of the partition with a node in the other part. Being an induced matching means that no two endpoints of any two edges of the matching are joined by a crossing edge.

Our construction of hard polynomials is based on the following lemma. Associate with every graph G = ([n], E) the multilinear polynomial  $f_G(x_1, \ldots, x_n)$  whose monomials are  $\prod_{i \in S} x_i$  over all subsets  $S \subseteq [n]$  such that the induced subgraph G[S] has an odd number of edges of G.

**Lemma 46.** For every non-empty graph G on n nodes, we have

$$A[f_G] \geqslant 2^{m(G)-2}.$$

We postpone the proof of this lemma and turn to its application.

The following simple claim gives us a general lower bound on the matching number m(G). Say that a graph is *s-mixed* if every two disjoint *s*-element subsets of its nodes are joined by at least one edge.

**Claim 47.** If an n-node graph G of maximum degree d is s-mixed, then  $m(G) \ge (\lfloor n/3 \rfloor - s)/(2d+1)$ .

*Proof.* Fix an arbitrary balanced partition of the nodes of G into two parts. To construct the desired induced matching, formed by crossing edges, we repeatedly take a crossing edge and remove it together with all its neighbors. At each step we remove at most 2d + 1 nodes. If the graph is s-mixed, then the procedure will run for m steps as long as  $\lfloor n/3 \rfloor - (2d+1)m$  is at least s.  $\square$ 

Thus, we need graphs of small degree that are still s-mixed for small s. Examples of such graphs are expander graphs. A Ramanujan (n,q)-graph is a regular graph G of degree q+1 on n nodes such that  $\lambda(G) \leq 2\sqrt{q}$ , where  $\lambda(G)$  is the second largest (in absolute value) eigenvalue of the adjacency matrix of G. Explicit constructions of Ramanujan (n,q)-graphs for every prime  $q \equiv 1 \mod 4$  and infinitely many values of n were given by Margulis [27], Lubotzky, Phillips and Sarnak [26]; these were later extended to the case where q is an arbitrary prime power by Morgenstern [31], and Jordan and Livné [16].

**Theorem 48.** If  $f_G(x_1,...,x_n)$  is the multilinear polynomial associated with a Ramanujan (n,64)-graph G, then

$$A[f_G] \geqslant 2^{0.001n}$$
.

*Proof.* The Expander Mixing Lemma ([2, Lemma 2.3]) implies that, if G is a d-regular graph on n nodes, and if  $s > \lambda(G) \cdot n/d$ , then G is s-mixed. Now, every Ramanujan (n,q)-graph G is d-regular with d = q + 1 and has  $\lambda(G) \le 2\sqrt{q}$ . Hence, the graph G is s-mixed for  $s = 2n/\sqrt{q} > 2\sqrt{q}n/(q+1)$ . In our case (for q = 64), we have that G is s-mixed for  $s = 2n/\sqrt{64} = n/4$ . Lemma 46 gives the desired lower bound.

It remains to prove Lemma 46.

Call polynomial  $f(x_1,...,x_n)$  a *product polynomial*, if f is a product of two polynomials on disjoint sets of variables, each of size at least n/3, that is, if f = g(Y)\*h(Z) for some partition  $Y \cup Z = \{x_1,...,x_n\}$  of variables with  $|Y|,|Z| \ge n/3$ , and some two polynomials g and h on these variables. Note that we do not require that, say, the polynomial g(Y) must depend on all variables in Y: some of them may have zero degrees in g.

**Claim 49** ([35]). If  $F(x_1,...,x_n)$  is a multilinear circuit of size s with  $n \ge 3$  input variables, then the polynomial F can be written as a sum of at most s+1 product polynomials.

*Proof.* Induction on s. For a gate u, let  $X_u$  be the set of variables in the corresponding subcircuit of F. Let v be the output gate of F. If v is an input gate, then F itself is a product polynomial, since  $n \geqslant 3$ . So, assume that v is not an input gate. If  $|X_v| \leqslant 2n/3$ , then the polynomial F itself is a product polynomial, because F = F\*1. So, assume that  $|X_v| > 2n/3$ . Every gate u in F entered by gates  $u_1$  and  $u_2$  admits  $|X_u| \leqslant |X_{u_1}| + |X_{u_2}|$ . Thus, there exists a gate u in F such that  $n/3 \leqslant |X_u| \leqslant 2n/3$ . By Lemma 31, we can write F as  $F = F_u + F_{u=0}$  where  $F_u = g_u*h$  with  $n/3 \leqslant |X_u| \leqslant 2n/3$  and some polynomial h. Moreover, since the circuit is multilinear, the set  $X_h$  of variables in the polynomial h must be disjoint from  $X_u$ , implying that  $|X_h| \geqslant n - |X_u| \geqslant n/3$ . Thus,  $g_u*h$  is a product polynomial. Since the circuit  $F_{u=0}$  has size at most s-1, the desired decomposition of F follows from the induction hypothesis.

By the *characteristic function* of a multilinear polynomial  $f(x_1, ..., x_n)$  we will mean the (unique) boolean function which accepts a binary vector  $a \in \{0,1\}^n$  if and only if the polynomial f contains the monomial  $x_1^{a_1} x_2^{a_2} \cdots x_n^{a_n} = \prod_{i: a_i=1} x_i$ . (Note that this boolean function needs not to be monotone.) In particular, the characteristic function of our polynomial  $f_G$  is the quadratic boolean function

$$\phi(x) = \sum_{\{i,j\} \in E} x_i x_j \mod 2.$$

That is,  $\phi(a) = 1$  if the subgraph G[S] induced by the set of nodes  $S = \{i : a_i = 1\}$  has an odd number of edges. Since  $\phi(x)$  is a non-zero polynomial of degree 2 over GF(2), we have that  $|f_G| = |\phi^{-1}(1)| \ge 2^{n-2}$ .

**Claim 50.** For every graph G on n nodes, every product sub-polynomial of  $f_G$  contains at most  $2^{n-m(G)}$  monomials.

*Proof.* Let G\*H be a product polynomial contained in  $f_G$ . This polynomial gives a partition x=(y,z) of the variables into two parts, each containing at least n/3 variables. Let g(y) and h(z) be the characteristic functions of G and H, and  $r(x)=g(y) \wedge h(z)$ . Then  $|G*H|=|r^{-1}(1)|$ , and it is enough to show that  $|r^{-1}(1)| \leq 2^{n-m(G)}$ . When doing this, we will essentially use the fact that  $r \leq \phi$ , which follows from the fact that all monomials of G\*H are also monomials of  $f_G$ .

By the definition of m(G), some set  $M = \{y_1 z_1, \dots, y_m z_m\}$  of m = m(G) crossing edges  $y_i z_i$  forms an induced matching of G. Given an assignment  $\alpha$  of constants 0 and 1 to the n - 2m variables outside the matching M, define vectors  $a, b \in \{0, 1\}^m$  and a constant  $c \in \{0, 1\}$  as follows:

- $a_i = 1$  iff an odd number of neighbors of  $y_i$  get value 1 under  $\alpha$ ,
- $b_i = 1$  iff an odd number of neighbors of  $z_i$  get value 1 under  $\alpha$ ,
- c = 1 iff the number of edges whose both endpoints get value 1 under  $\alpha$  is odd.

Then the subfunction  $\phi_{\alpha}$  of  $\phi$  obtained after restriction  $\alpha$  is

$$\phi_{\alpha}(y_1, \dots, y_m, z_1, \dots, z_m) = \sum_{i=1}^m y_i z_i + \sum_{i=1}^m y_i a_i + \sum_{i=1}^m b_i z_i + c \mod 2$$
$$= IP_m(y \oplus b, z \oplus a) \oplus IP_m(a, b) \oplus c,$$

where  $IP_n(y_1, \ldots, y_m, z_1, \ldots, z_m) = \sum_{i=1}^m y_i z_i \mod 2$  is the inner product function (scalar product). Since a, b and c are fixed, the corresponding  $2^m \times 2^m \pm 1$  matrix H with entries  $H[y, z] = (-1)^{\phi_\alpha(y, z)}$  is a Hadamard matrix (rows are orthogonal to each other). Lindsey's Lemma (see, e.g. [19, p. 479]) implies that no monochromatic submatrix of H can have more than  $2^m$  1-entries.

Now, the obtained subfunction  $r_{\alpha} = g_{\alpha}(y_1, \dots, y_m) \wedge h_{\alpha}(z_1, \dots, z_m)$  of  $r = g(y) \wedge h(z)$  also satisfies  $r_{\alpha}(a,b) \leqslant \phi_{\alpha}(a,b)$  for all  $a,b \in \{0,1\}^m$ . Since the set of all pairs (a,b) for which  $r_{\alpha}(a,b) = 1$  forms a *submatrix* of H, this implies that  $r_{\alpha}$  can accept at most  $2^m$  such pairs. Since this holds for each of the  $2^{n-2m}$  assignments  $\alpha$ , the desired upper bound  $|r^{-1}(1)| \leqslant 2^m \cdot 2^{n-2m} = 2^{n-m}$  follows.

This completes the proof of Claim 50, and hence, the proof of Lemma 46.

### 13 Bounds on Circuit Depth

So far, we were interested in the *size* of circuits. Another important measure is the circuit *depth*, i.e. the maximum number of gates in an input-output path.

#### 13.1 Upper Bounds

If a polynomial f can be produced by a circuit of  $size\ s$ , what is then the smallest depth of a circuit producing f? Hyafil [14] has shown that then f can be also produced by a circuit of depth proportional to  $(\log d)(\log sd)$ , where d is the maximum degree of f. (This can be easily shown by induction on the degree using the decomposition given in Lemma 38.) However, the size of the resulting circuit may be as large as  $s^{\log d}$ . A better simulation, leaving the size polynomial in s, was found by Valiant et al. [44].

**Theorem 51** (Valiant et al. [44]). If a polynomial f of maximum degree d can be produced by a circuit of size s, then f can be also produced by a circuit of size  $O(s^3)$  and depth  $O(\log s \log d)$ .

Important is that the proof of Theorem 51 in [44] is constructive: the new (small-depth) circuit can be *constructed* from a given circuit F. The parameter d is then the maximum degree of the polynomial F produced by F. This has, for example, interesting consequences for the st-connectivity polynomial  $f = STCON_n$ . Using binary search, one can easily construct a Min-circuit of depth  $O(\log^2 n)$  for f. But the size of the resulting circuit will then be  $n^{\Omega(\log n)}$ . To get a circuit of depth  $O(\log^2 n)$  but polynomial size, we can take the circuit F of size  $O(n^3)$  resulting from the Bellman–Ford dynamic programming algorithm (see Theorem 4). It is easy to see that the produced polynomial F in this case has maximum degree  $d \leq n$ . Thus, Theorem 51 gives us a Min-circuit for STCONn which simultaneously has depth  $O(\log^2 n)$  and size  $O(n^9)$ .

#### 13.2 Lower Bounds

We now turn to proving lower bounds on the depth of circuits. Lemma 15 implies that the smallest depth of a monotone boolean circuit computing a polynomial f is a lower bound on the depth of any circuit computing f over any semiring of zero characteristic, including the arithmetic semiring A as well as tropical semirings Min, Min $^-$  and Max $^*$ . As shown by Karchmer and Wigderson [20], lower bounds on the depth of monotone (as well as non-monotone) boolean circuit depth can be obtained via communication complexity arguments. However, applications of these arguments for specific polynomials are usually rather involved. On the other hand, lower bounds on the depth of monotone arithmetic circuits are much easier to obtain.

In the previous sections, we have shown that the factor-density measure  $\#_r(f)$  can be used to lower bound the circuit size. By simplifying previous arguments of Shamir and Snir [40], Tiwari and Tompa [42] have shown that the measure  $\#_r(f)$  can be used to lower bound the circuit depth as well. The idea was demonstrated in [42] on two applications (Corollaries 54 and 55 below). Here we put their idea in a general frame.

A subadditive weighting of a circuit F is an assignment  $\mu : F \to R_+$  of non-negative weights to the gates of F such that the output gate gets weight  $\geqslant 1$ , all other gates get weight  $\leqslant 1$ , and  $\mu(v+w) \leqslant \mu(v) + \mu(w)$  holds for every sum gate v+w. Given such a weighting, define the decrease  $K_u$  at a product gate u=v\*w as

$$K_u = \frac{\mu(v) \cdot \mu(w)}{\mu(u)} .$$

Note that, since  $\mu(v) \leq 1$  holds for every non-output gate v, we have

$$\mu(u) \leqslant \frac{1}{K_u} \cdot \min\{\mu(v), \mu(w)\}.$$

That is, when entering u from any of its two inputs, the weight must decrease by a factor of at least  $K_u$ . This explains the use of term "decrease". Let  $K_{r,s} = \min_u K_u$  be the smallest decrease at a product gate u of degree r, one of whose inputs has degree s; by the *degree* of a gate we mean the minimum degree of the polynomial produced at that gate.

**Lemma 52.** Let F be a circuit, whose produced polynomial has minimum degree d, and let  $m = \lfloor \log_2 d \rfloor$ . Then, for every subadditive weighting, there is a sequence  $d = r_0 > r_1 > \ldots > r_m = 1$  of integers such that  $r_{i+1} \geqslant \frac{1}{2}r_i$  for all  $i = 1, \ldots, m$ , and the circuit F has depth at least

$$m + \log_2 \prod_{i=0}^{m-1} K_{r_i, r_{i+1}}$$
.

*Proof.* Construct a path  $\pi$  from the output gate to an input gates as follows: at a sum gate choose the input of greater weight, and at a product gate choose an input of greater degree. Since the produced polynomial has minimum degree d, and since at each product gate we chose an input of greater degree, there must be at least m product gates along  $\pi$ . Let  $d = r_1 > r_2 > \ldots > r_m > r_{m+1} = 1$  be the degrees of the product gates (and input node) on path  $\pi$ . Let  $k_i = K_{r_i, r_{i+1}}$  be the decrease of the i-th product gate along  $\pi$ . Note by the construction of  $\pi$  that  $r_{i+1} \geqslant \frac{1}{2}r_i$ .

Let us now view the path  $\pi$  in the reversed order (from input to output). So, we start with some gate of weight  $\leq 1$  (an input gate). Since the weighting is subadditive, at each edge entering a sum gate the weight can only increase by a factor of at most 2. So, if s is the number of sum gates along  $\pi$ , then the total increase in weight is by a factor at most  $2^s$ . But when entering the i-th product gate,

the weight decreases by a factor at least  $k_i$ . Thus, the total loss in the weight is by a factor at least  $\prod_{i=0}^{m-1} k_i$ . Since the last (output) gate must have weight  $\geqslant 1$ , this gives

$$2^s \cdot \prod_{i=0}^{m-1} \frac{1}{k_i} \geqslant 1$$
, and hence,  $s \geqslant \log_2 \prod_{i=0}^{m-1} k_i$ .

Since the depth of F is at least the length of m + s of the path  $\pi$ , we are done.

We now give a specific weighting, based on the factor-density measure  $\#_r(f)$ . Recall that  $\#_r(f)$  is the maximum number of monomials in f containing a fixed monomial of degree r as a common factor. For a polynomial f of minimum degree d, and integers  $1 \le s < r \le d$ , define

$$K_f(r,s) = \frac{\#_{d-r}(f)}{\#_{d-s}(f) \cdot \#_{d-r+s}(f)}$$
.

Note that we have already used this measure to lower-bound the size of circuits: if f is a homogeneous polynomial of degree d, then Lemma 42 yields  $A[f] \ge K_f(d,s)$  for some  $d/3 \le s \le 2d/3$ .

For a polynomial f, let Depth[f] denote the smallest possible depth of a circuit producing f.

**Lemma 53.** Let f be a polynomial of minimum degree d, and  $m = \lfloor \log_2 d \rfloor$ . Then there is a sequence  $d = r_0 > r_1 > \ldots > r_m = 1$  of integers such that  $r_{i+1} \geqslant \frac{1}{2}r_i$  for all  $i = 1, \ldots, m$ , and

Depth[f] 
$$\geqslant m + \log_2 \prod_{i=0}^{m-1} K_f(r_i, r_{i+1})$$
.

*Proof.* Let F be a circuit producing f; hence, F = f. For a gate  $u \in F$ , let  $d_u$  be the minimum degree of the polynomial produced at u. By Theorem 31, we know that F can be written as a sum  $F = f_u * \overline{f}_u + F_{u=0}$ , where  $f_u$  is the polynomial produced at gate u. Since  $f_u * \overline{f}_u \subseteq f$ , and  $f_u$  has minimum degree  $d_u$ , the polynomial  $\overline{f}_u$  must contain a monomial of degree  $\geqslant d - d_u$ . Hence, by Observation 41, we have that  $|f_u| \leqslant \#_{d-d_u}(f)$ . This suggests the following weighting of gates:

$$\mu(u) = \frac{|f_u|}{\#_{d-d_u}(f)}.$$

The output gate v then gets weight  $\mu(v) = |f|/\#_{d-d}(f) = 1$ , whereas all other gates get weights  $\leq 1$ . Moreover, since for every product gate u = v\*w, we have that  $|f_u| = |f_v| \cdot |f_w|$  and  $d_u = d_v + d_w$ , the decrease  $K_{r,s}$  of this weighting coincides with  $K_f(r,s)$ . So, it remains to show that the weighting is subadditive.

To show this, let u = v + w be a sum gate. Then  $d_u = \min\{d_v, d_w\}$ , and hence,  $d - d_u = \max\{d - d_v, d - d_w\}$ . So,

$$\mu(v+w) = \frac{|f_v| + |f_w|}{\#_{d-d_u}(f)} = \frac{|f_v| + |f_w|}{\max\{\#_{d-d_v}(f), \#_{d-d_w}(f)\}} \le \mu(v) + \mu(w).$$

**Corollary 54** ([40, 42]). If  $f = PER_n$ , then  $Depth[f] \ge n + \lfloor \log_2 n \rfloor - 1$ .

*Proof.* The permanent polynomial  $f = PER_n$  is a homogeneous multilinear polynomial of degree d = n. Moreover,  $\#_l(f) = (n - l)!$  holds for any  $1 \le l \le d$ . Hence,

$$K_f(r,s) = \frac{r!}{s!(r-s)!} = \binom{r}{s}.$$

But  $r_{i+1} \geqslant \frac{1}{2}r_i$  implies that  $\binom{r_i}{r_{i+1}} \geqslant 2^{r_i-r_{i+1}}$ . Hence,

$$\prod_{i=0}^{m-1} K_f(r_i, r_{i+1}) = \prod_{i=0}^{m-1} \binom{r_i}{r_{i+1}} \geqslant 2^{r_0 - r_m} = 2^{n-1}.$$

This lower bound for f = PER is not very surprising, since Depth[f] is always at least logarithmic in A[f], and we already know (Theorem 43) that A[f] is exponential for this polynomial. More interesting, however, is that the argument above allows to prove super-logarithmic depth lower bounds even for polynomials that *have* circuits of polynomial size.

To demonstrate this, consider the following *layered st-connectivity polynomial*  $f_{n,d}$ . The monomials of this polynomial correspond to st-paths in a layered graph. We have d+1 disjoint layers, where the first contains only one node s, the last only one node t, and each of the remaining d-1 layers contains n nodes. Monomials of  $f_{n,d}$  have the form  $x_{s,a_1}x_{a_1,a_2}\cdots x_{a_{d-2},a_{d-1}}x_{a_{d-1},t}$  with  $a_i$  belonging to the i-th layer. In other words, this polynomial corresponds to computing the (s,t)-entry of the product of d-2 matrices of dimension  $n \times n$ . Hence, it can be produced by a circuit of depth  $O((\log d)(\log n))$ .

**Corollary 55** ([40, 42]). Depth[ $f_{n,d}$ ] =  $\Omega(\log d \cdot \log n)$ .

*Proof.* The polynomial  $f = f_{n,d}$  is a multilinear homogeneous polynomial of degree d with  $|f| = n^{d-1}$  monomials. To estimate the factor-density  $\#_l(f)$ , let us fix a set E of |E| = l edges. Every edge  $e \in E$  constrains either two inner nodes (if  $s, t \notin e$ ) or one inner node. Thus, if we fix l edges, then at least l inner nodes are constrained, implying that only  $\#_l(f) \leqslant n^{d-1-l}$  paths can contain all these edges. In fact, we have an equality  $\#_l(f) = n^{d-1-l}$ : every monomial  $x_{s,a_1}x_{a_1,a_2}\cdots x_{a_{l-1},a_l}$  consisting of initial l edges is a factor of exactly  $n^{d-1-l}$  monomials of f. Thus, the decrease in this case is

$$K_f(r,s) = \frac{\#_{d-r}(f)}{\#_{d-s}(f) \cdot \#_{d-(r-s)}(f)} = \frac{n^{r-1}}{n^{s-1} \cdot n^{r-s-1}} = n$$

for all  $1 \le s < r \le d$ . Lemma 53 yields  $\operatorname{Depth}[f] \geqslant \log_2 d + \log_2 n^{\lfloor \log_2 d \rfloor}$ , as desired.  $\square$ 

Corollary 55 implies an  $\Omega(\ln^2 n)$  lower bound on the depth of monotone arithmetic circuits computing STCON<sub>n</sub>: this polynomial can be obtained from  $f_{m,m-1}$  with  $m = \Theta(n^2)$  by setting to 0 some of the variables. Moreover, since  $f_{m,m-1}$  is multilinear and homogeneous, Theorem 13 implies that this lower bound holds also in all four tropical semirings.

Note that this bound for STCON is not new: together with Lemma 15, they follow from known lower bounds on the depth of monotone boolean circuits for these polynomials. The lower bound  $\Theta(\log^2 n)$  for STCON<sub>n</sub> was proved by Karchmer and Wigderson [20], and the lower bound  $\Omega(\ln^2 n/\ln\ln n)$  for CONN<sub>n</sub> was proved by Goldmann and Håstad [13]; Yao [47] earlier proved  $\Omega(\ln^{3/2} n/\ln\ln n)$  for this latter polynomial. However, the proofs for boolean circuits are much more involved than the proof for tropical circuits given above.

Bound	Property of $f$	Ref.
B(f) > t	f is not t-simple (Def. 16)	Thm. 17
S(f) = A[f]	f is homogeneous and multilinear	Thm. 13
$A[f] \geqslant  f $	f is separated (Def. 22)	Thm. 24
$A[f] \geqslant \frac{ f }{2kl^2}$ $A[f] \geqslant \frac{ f }{\#_r(f) \cdot \#_{d-r}(f)}$	$A*B \subseteq f \text{ implies }  A  \leqslant l \text{ or }  B  \leqslant k$	Thm. 34
$A[f] \geqslant \frac{ f }{\#_r(f) \cdot \#_{d-r}(f)}$	f of minimum degree $d$	Lem. 42

Table 2: A summary of general lower bounds. Here S is an arbitrary tropical semiring,  $\#_r(f)$  is the maximum possible number of monomials of f containing a fixed monomial of degree r, and r is some integer  $m/3 \le r \le 2m/3$ .

#### 14 Conclusion

As mentioned in the introduction, the model of tropical circuits is important because of its intimate relation with dynamic programming. The first goal of this paper was to relate tropical circuits with monotone arithmetic circuits, the later model being one of the most restricted ones. This is done in Theorem 13: if a polynomial f is homogeneous and multilinear, then the smallest size of tropical circuits computing f just coincides with A[f], the smallest size of a monotone arithmetic circuit producing f. We then presented several known and new methods to prove lower bounds on A[f] (see Table 1). These allow to relatively easily prove strong lower bounds for tropical circuits; Table 2 gives a short overview.

Still, these arguments seem to fail for non-homogeneous polynomials like CONN or STCON. By Theorem 45, we know that Max- and Min<sup>-</sup>-circuits for these polynomials must have exponential size. But boolean and Min-circuits can compute these polynomials in size  $O(n^3)$  (see Theorem 2). So, the main problem left open in this paper is:

Does B(f) = 
$$\Omega(n^3)$$
 or at least Min(f) =  $\Omega(n^3)$  hold for  $f = STCON_n$  and/or  $f = CONN_n$ ?

Note that the lower bound  $\Omega(n^3)$  for the all-pairs shortest paths polynomial APSP, given in Corollary 28 does not automatically imply the same lower bound for the connectivity polynomial CONN: a circuit for CONN needs *not* to compute the polynomials of APSP at *separate* gates.

Even the model of monotone arithmetic circuits remains not well understood. Known methods are able to yield strong lower bounds only for polynomials f that are "resistant against products": no large subset of monomials of f can be represented by a product of two polynomials of sufficiently large degree. These methods, however, automatically fail for polynomials which themselves are products of polynomials. Say, we already know that the triangle polynomial  $f(x, y, z) = \sum_{i,j \in [n]} z_{ij} f_{ij}$  with  $f_{ij} = \sum_{k \in [n]} x_{ik} y_{kj}$  has  $A[f] = \Theta(n^3)$ . Replace now the outer sum by product, and consider the polynomial  $g(x, y, z) = \prod_{i,j \in [n]} z_{ij} \sum_{k \in [n]} x_{ik} y_{kj}$ , a product of  $n^2$  polynomials of degree three. Does  $A[g] = \Omega(n^3)$ ?

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